ANNOTATED SHEET SHOWING CHANGES

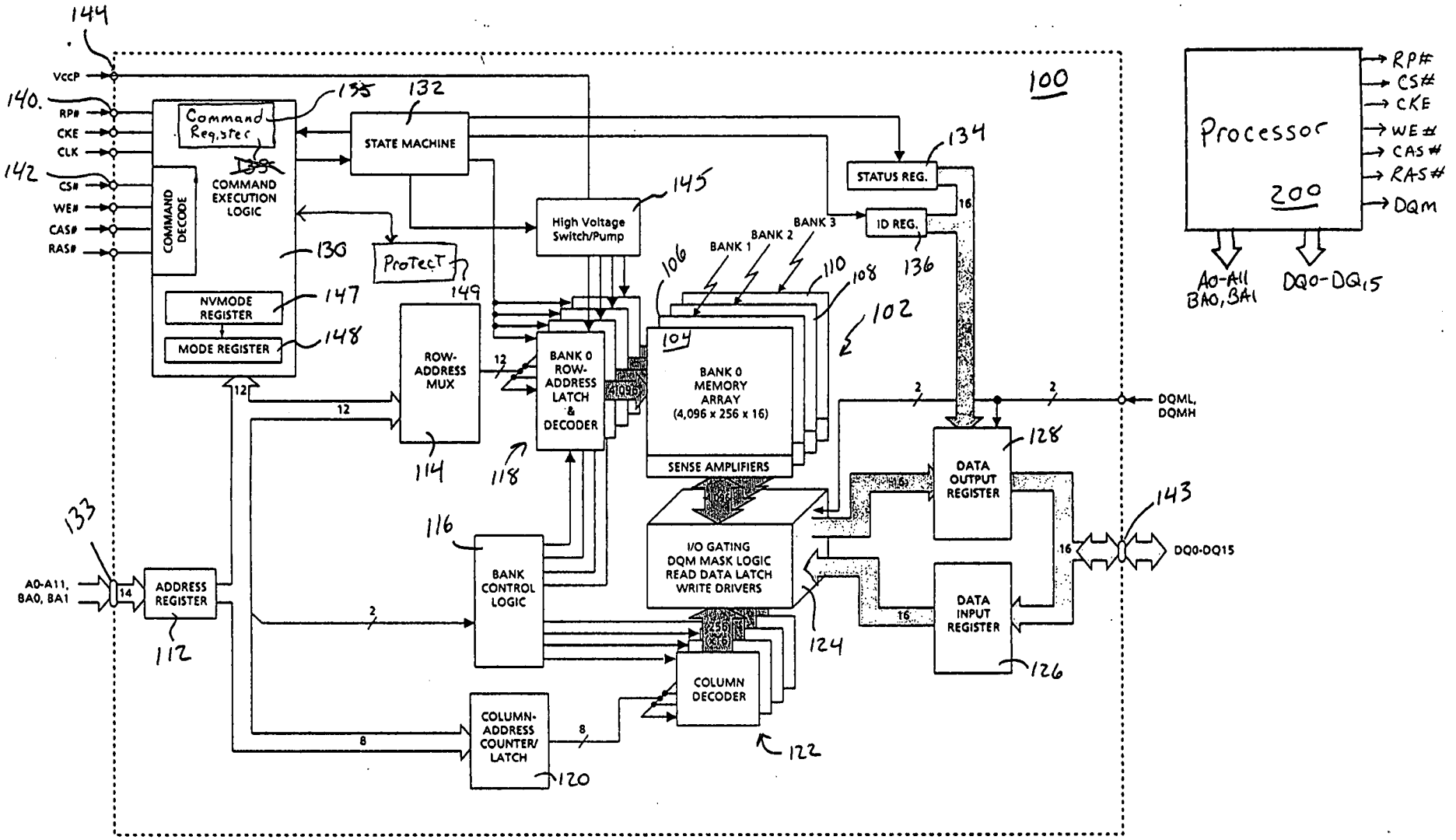


Fig. 1A

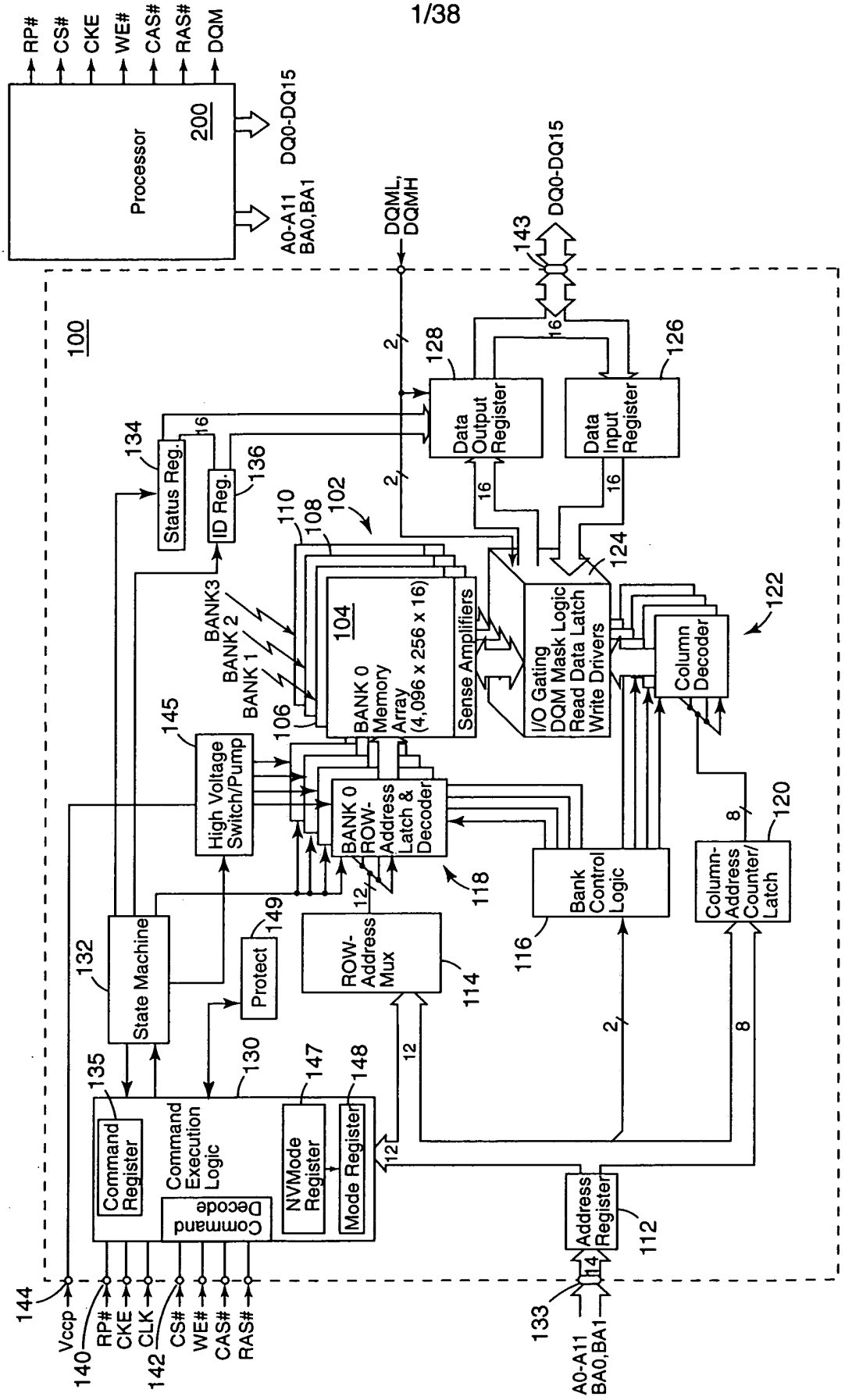


Fig. 1A

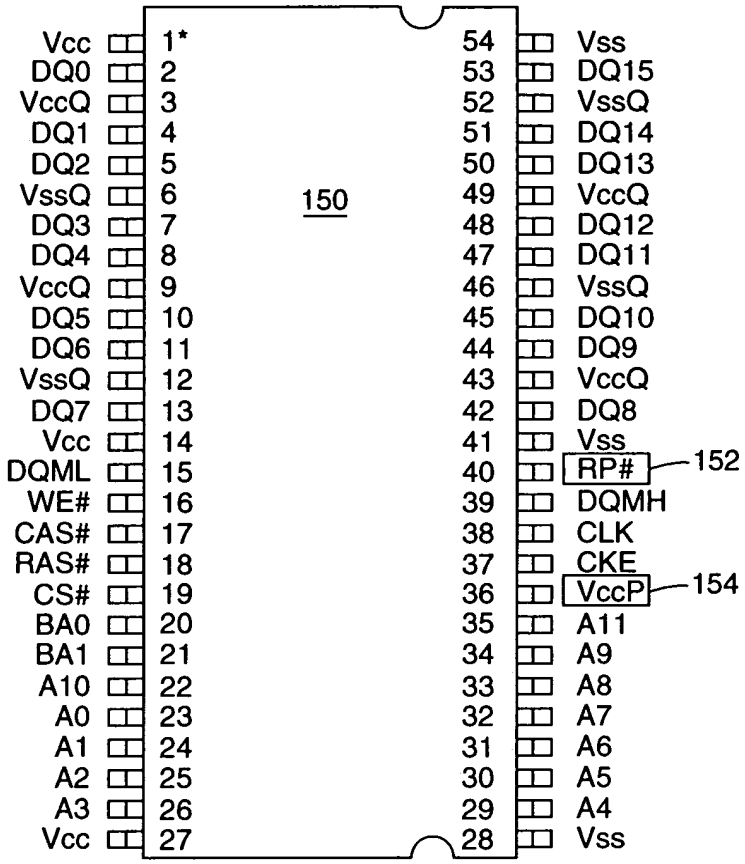


Fig. 1B

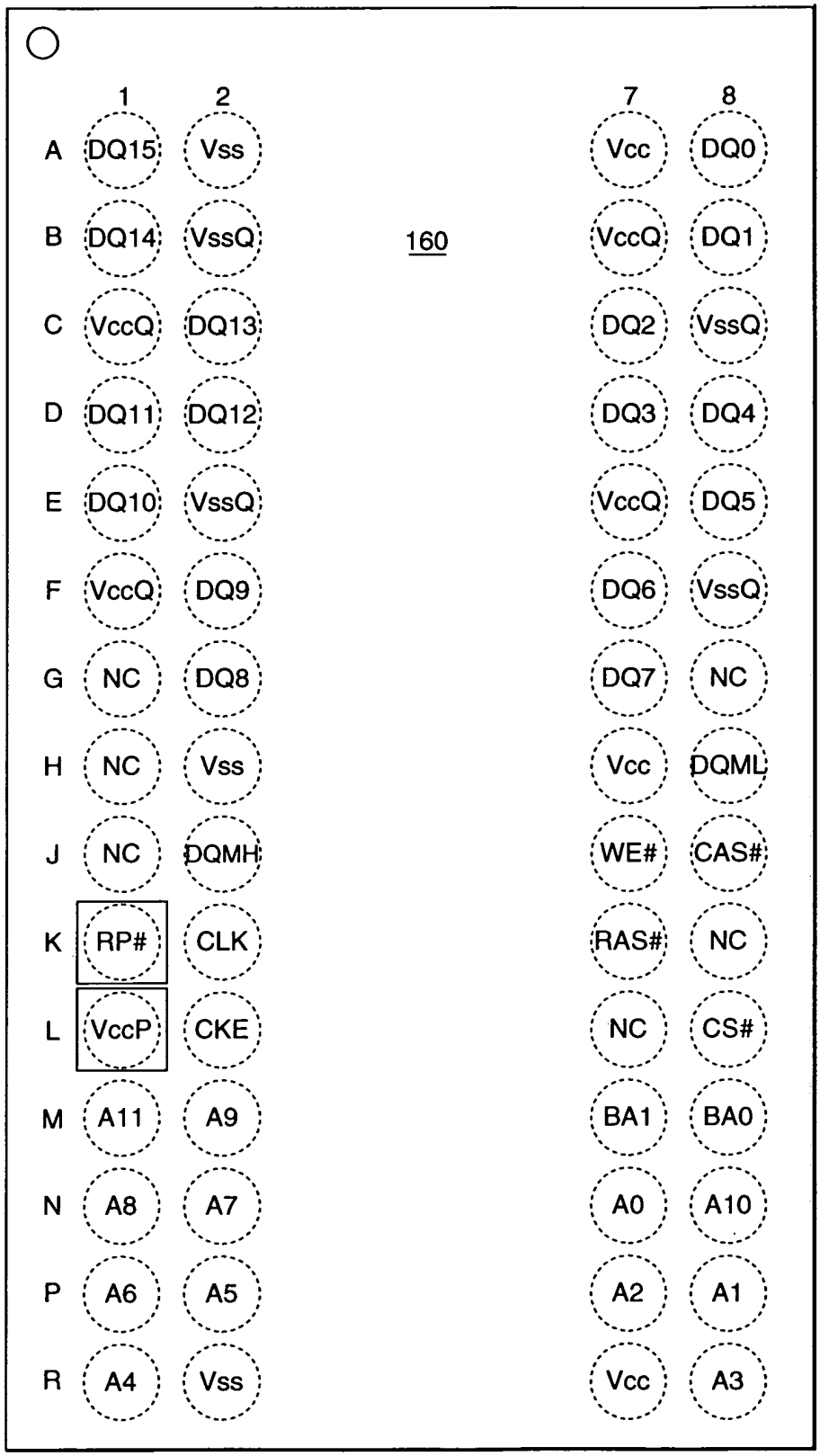


Fig. 1C

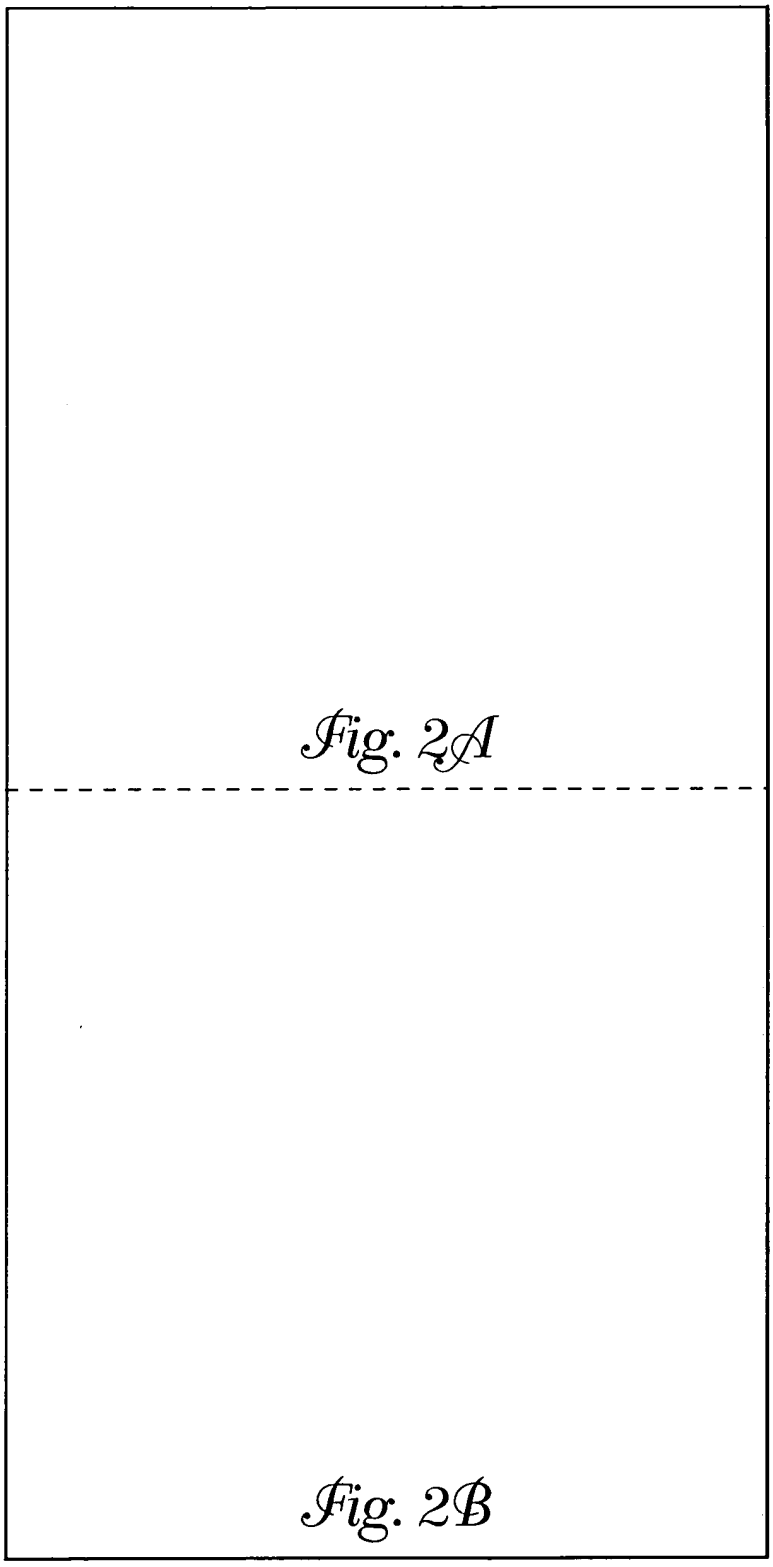


Fig. 2A

Fig. 2B

Fig. 2

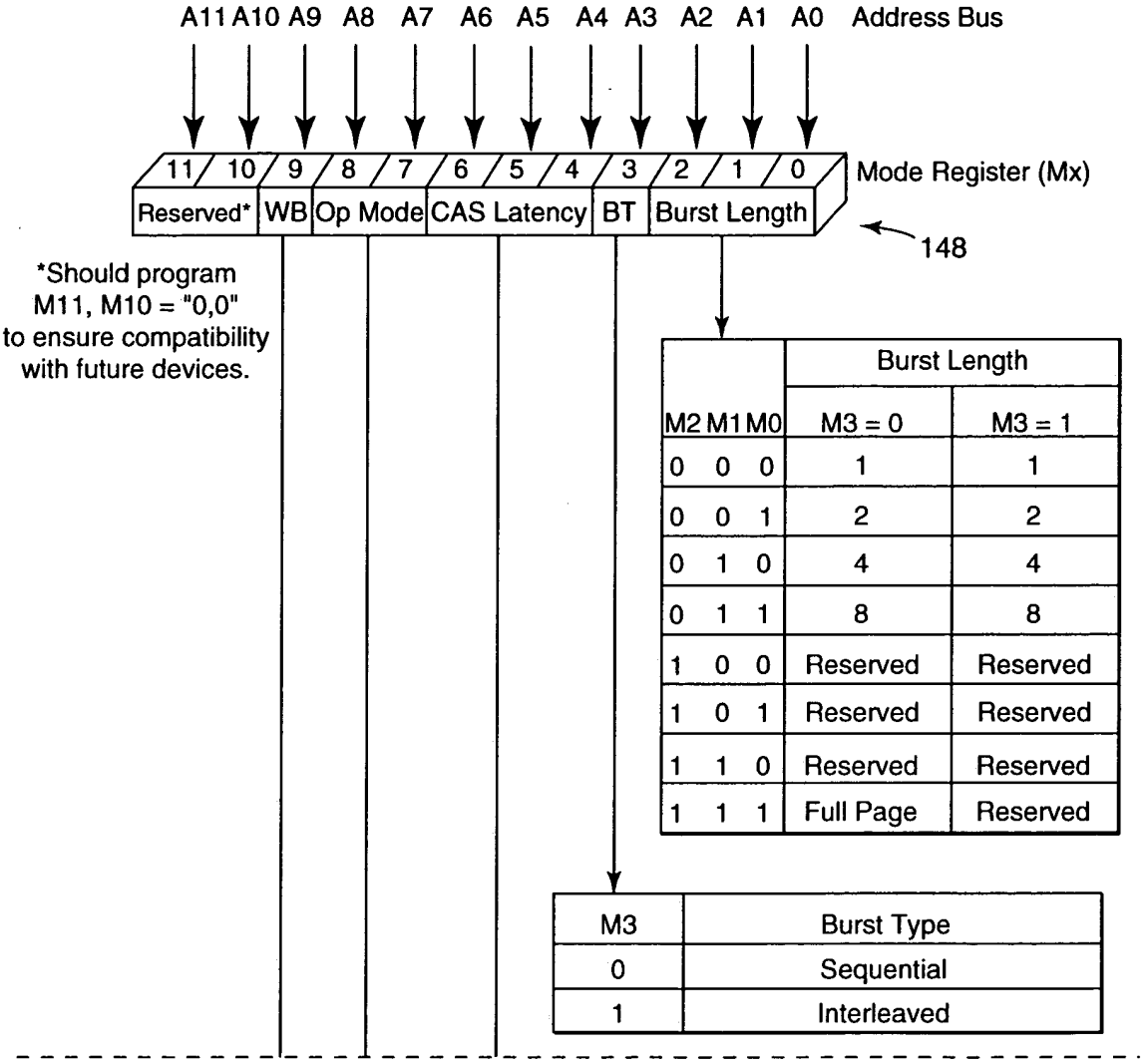


Fig. 2A

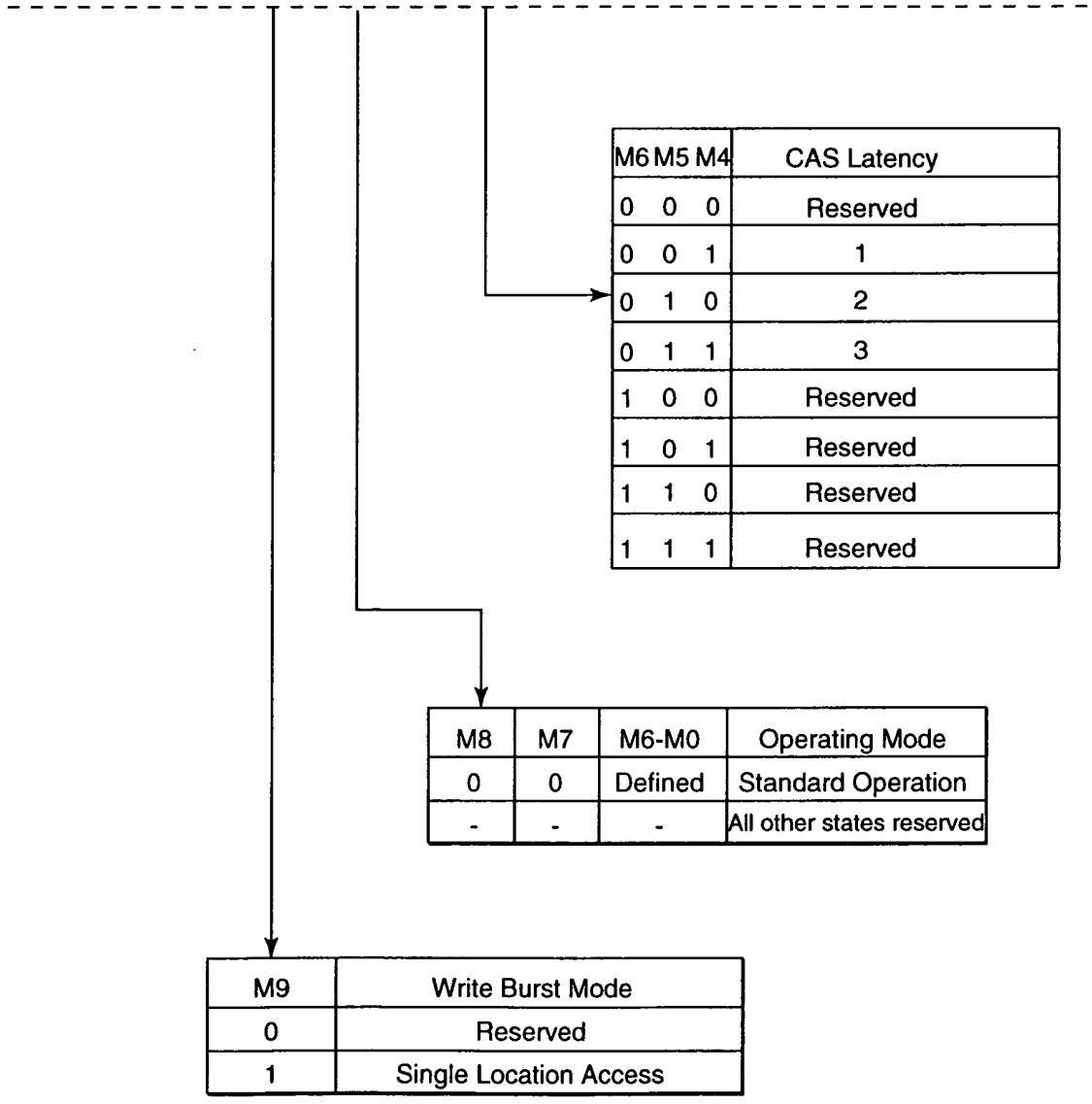


Fig. 2B

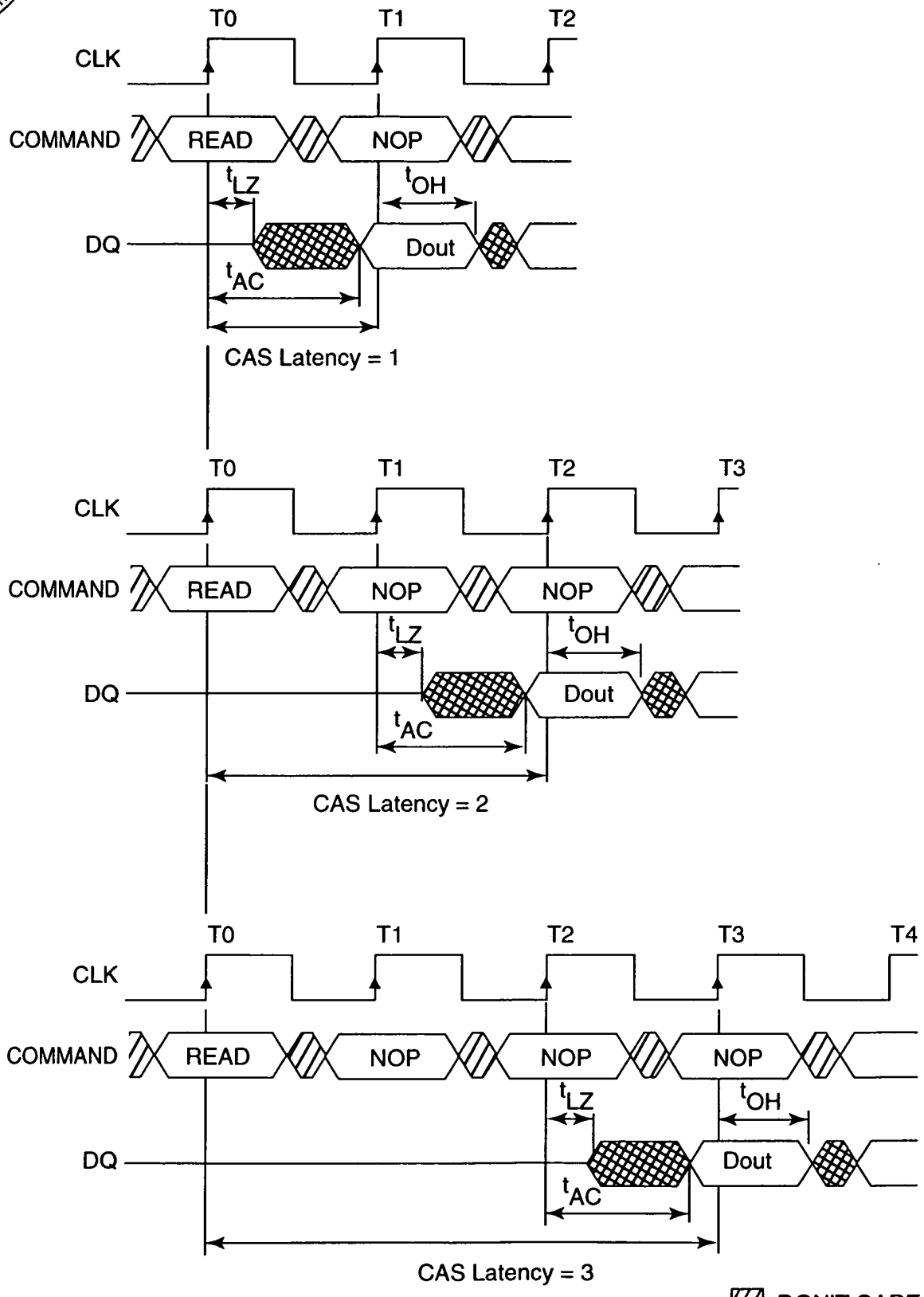


Fig. 3

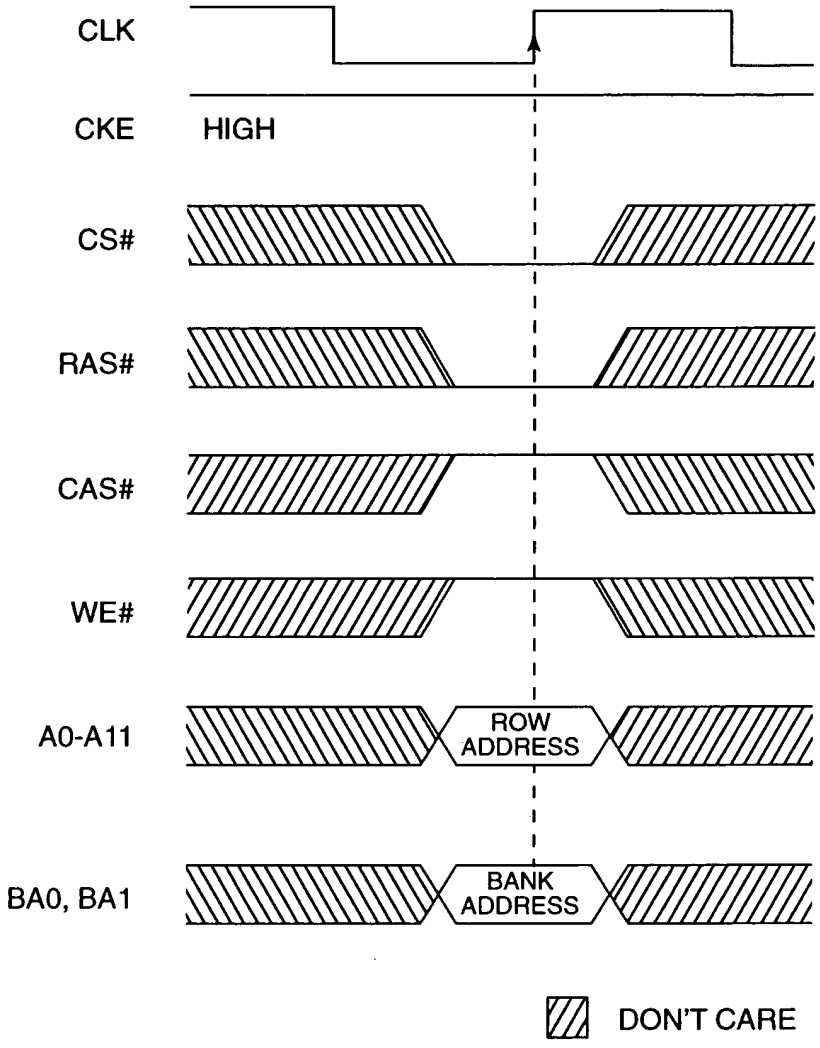


Fig. 4

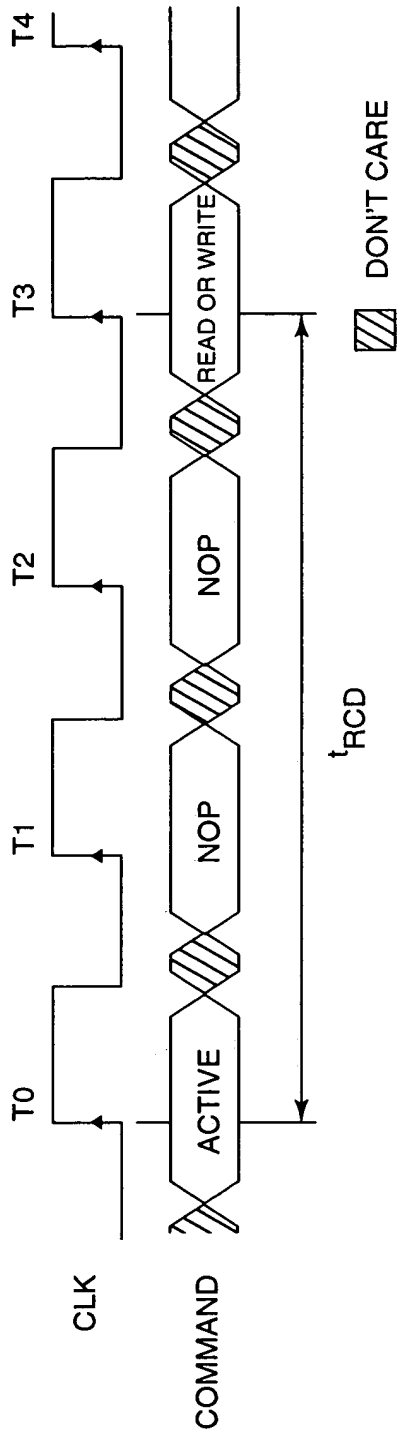


Fig. 5

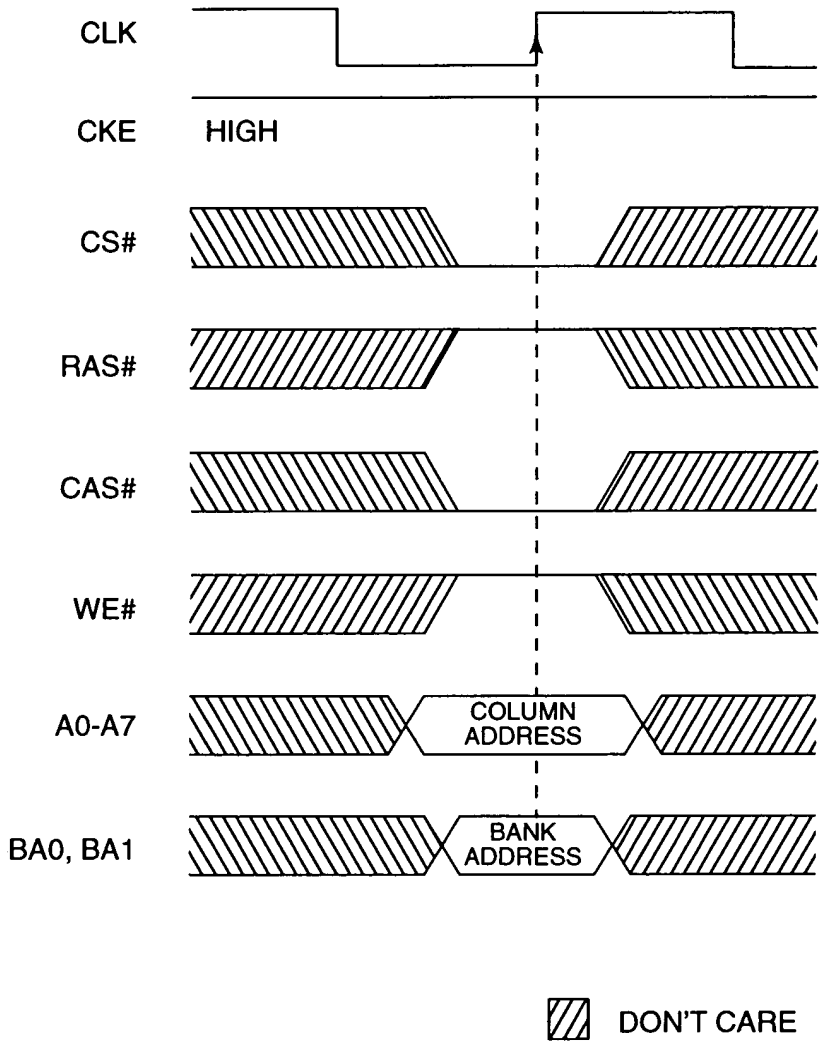
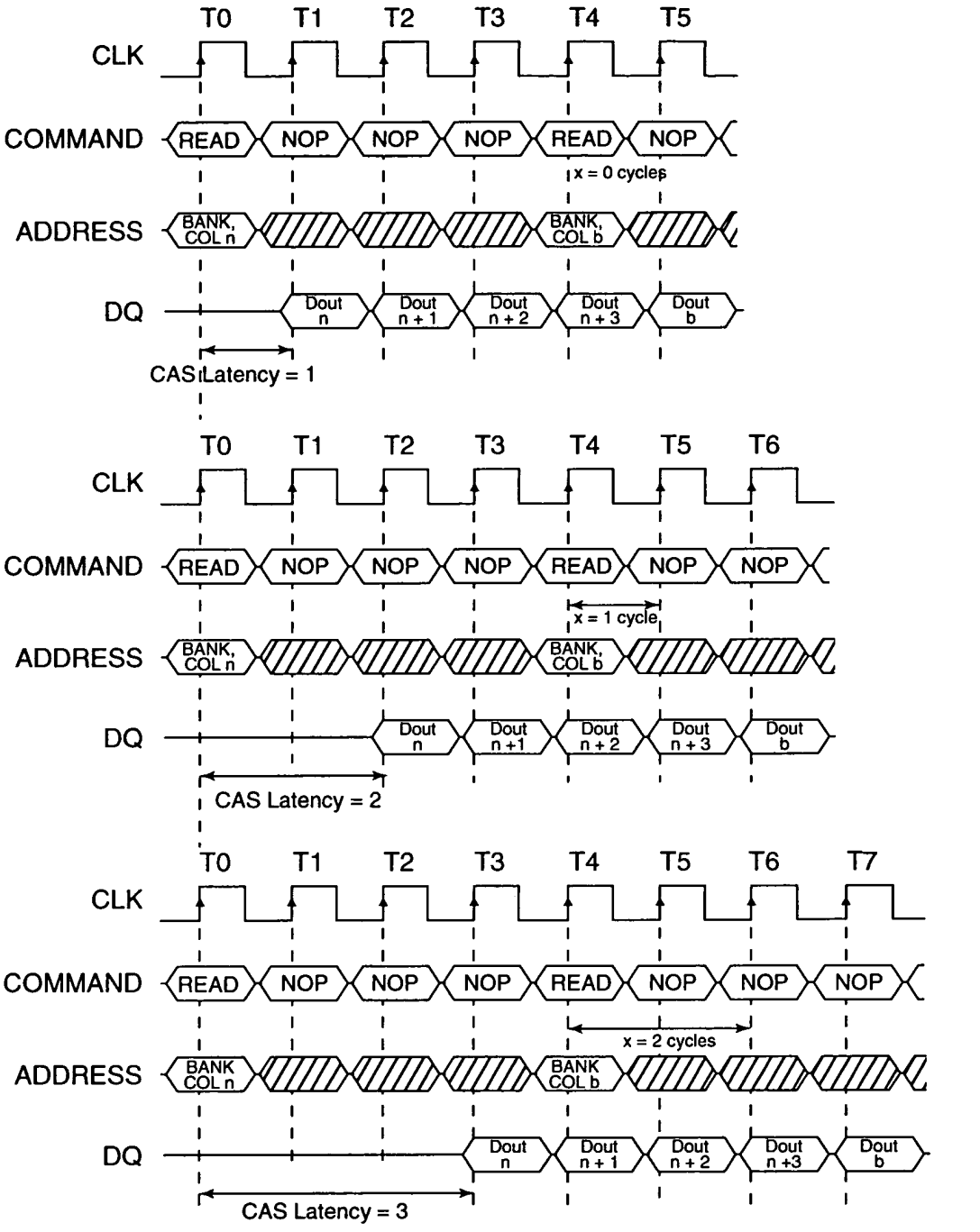
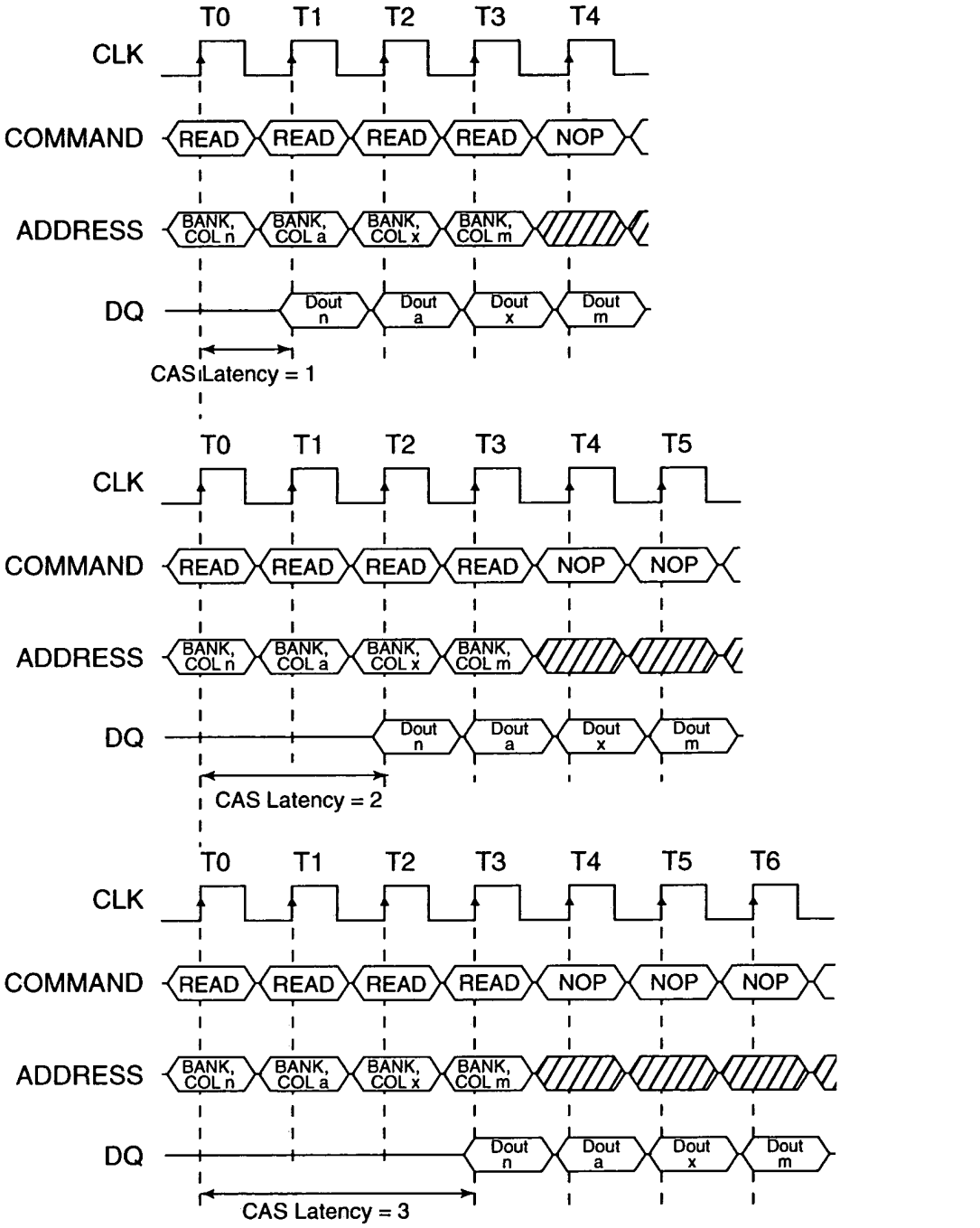


Fig. 6



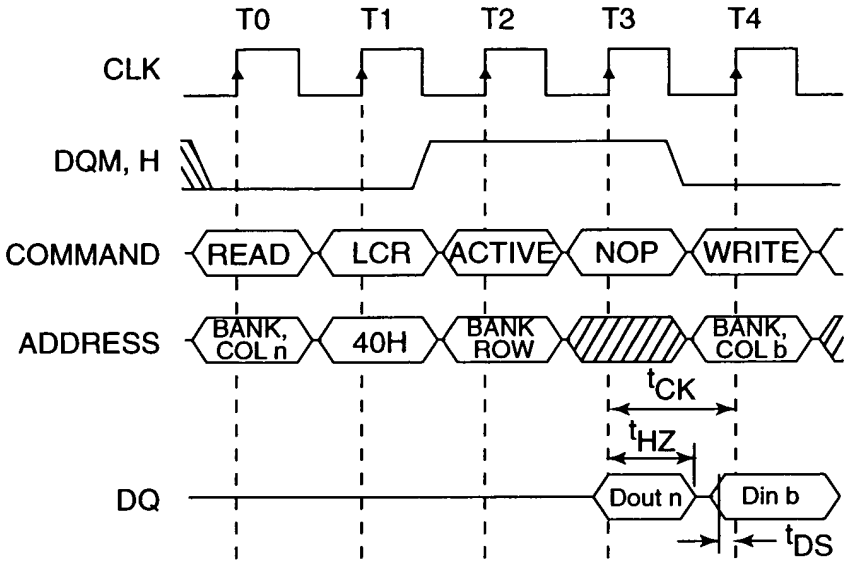
NOTE: Each READ command may be to either bank. DQM is LOW.

Fig. 7



NOTE: Each READ command may be to either bank. DQM is LOW.

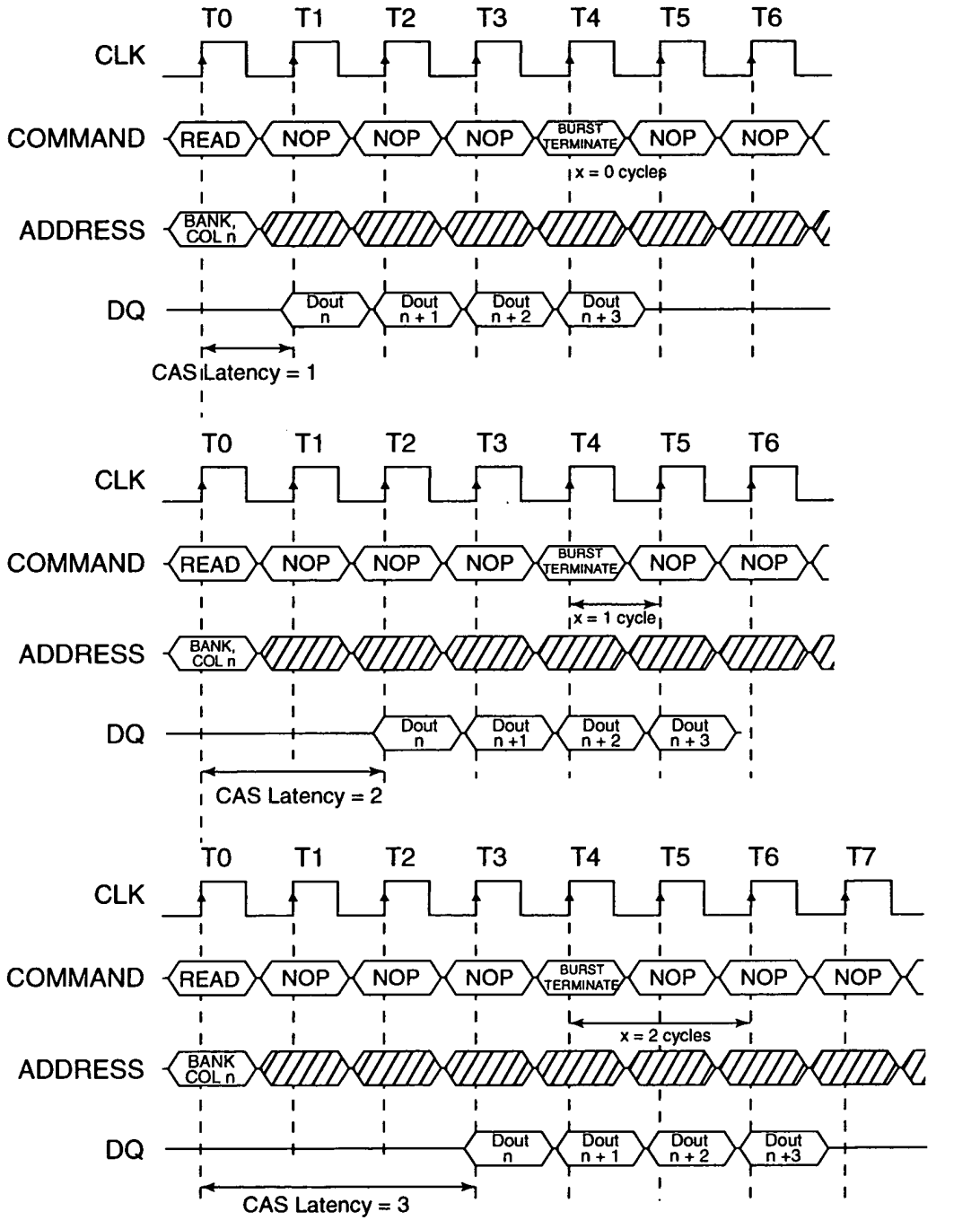
Fig. 8



NOTE: A CAS latency of three is used for illustration. The READ command may be to any bank, and the WRITE command may be to any bank. If a CAS latency of one is used, then DQM is not required.

▨ DON'T CARE

Fig. 9



NOTE: DQM is LOW.

Fig. 10

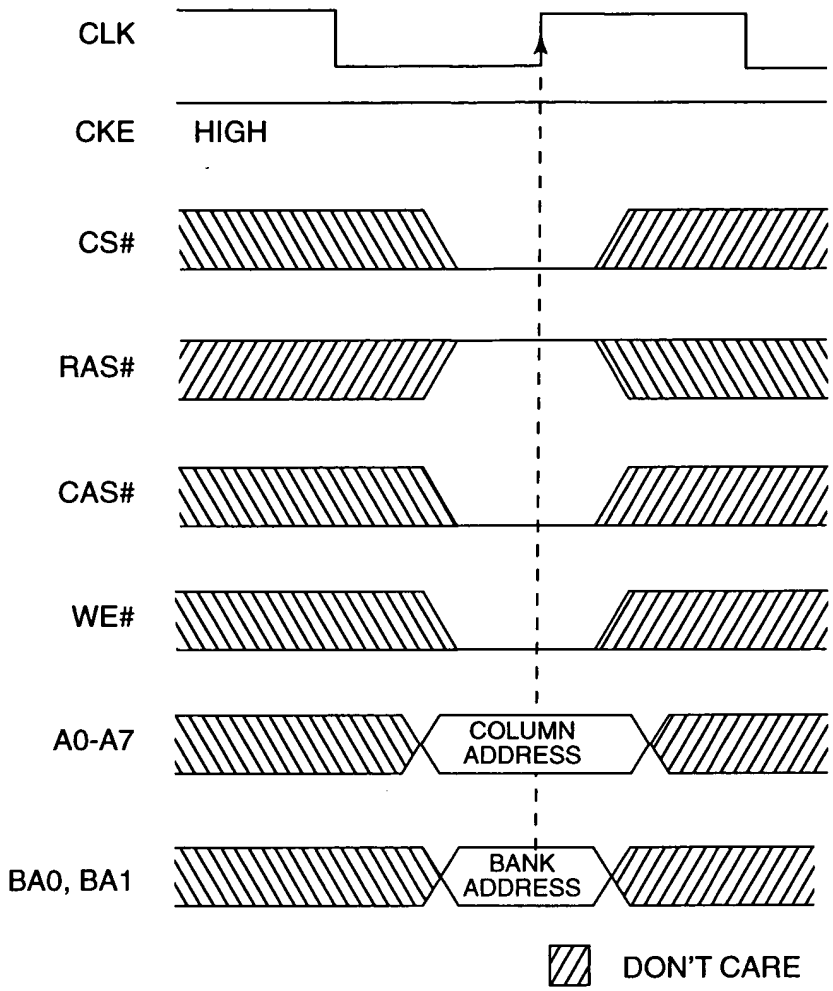
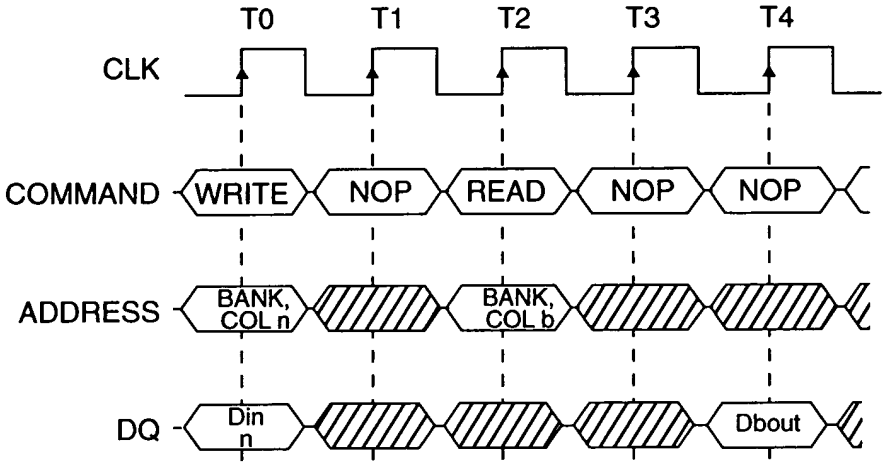


Fig. 11



NOTE: A CAS latency of two is used for illustration. The WRITE command may be to any bank and the READ command may be to any bank. DQM is LOW. A READ to the bank undergoing the WRITE ISM operation may output invalid data.

▨ DON'T CARE

Fig. 12

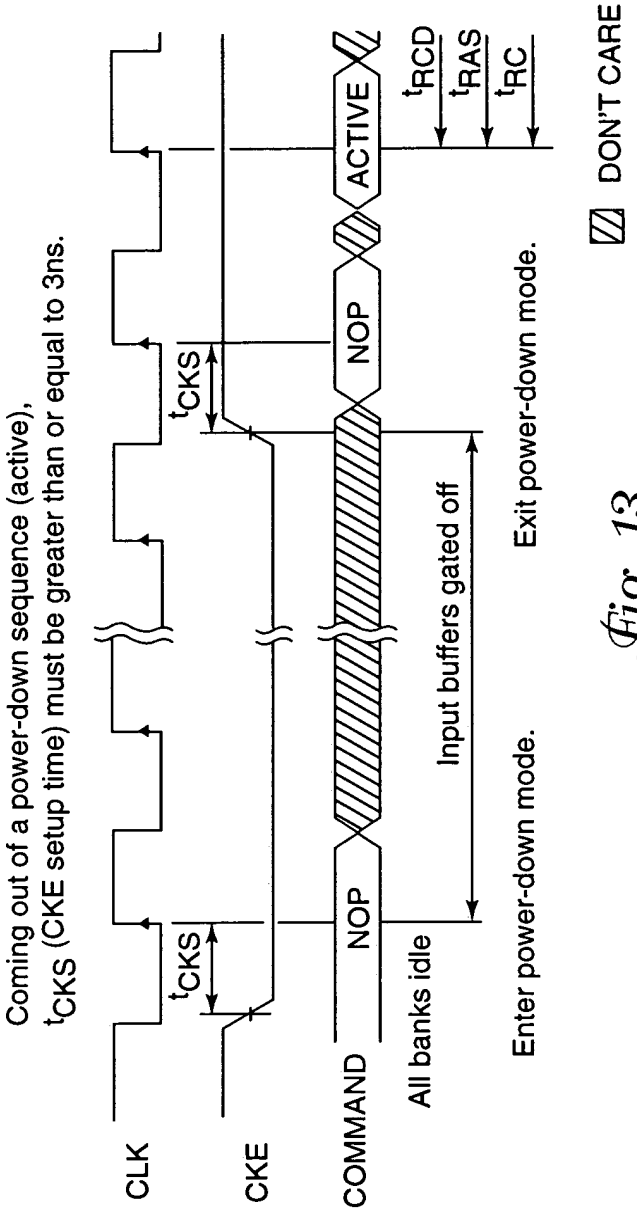


Fig. 13

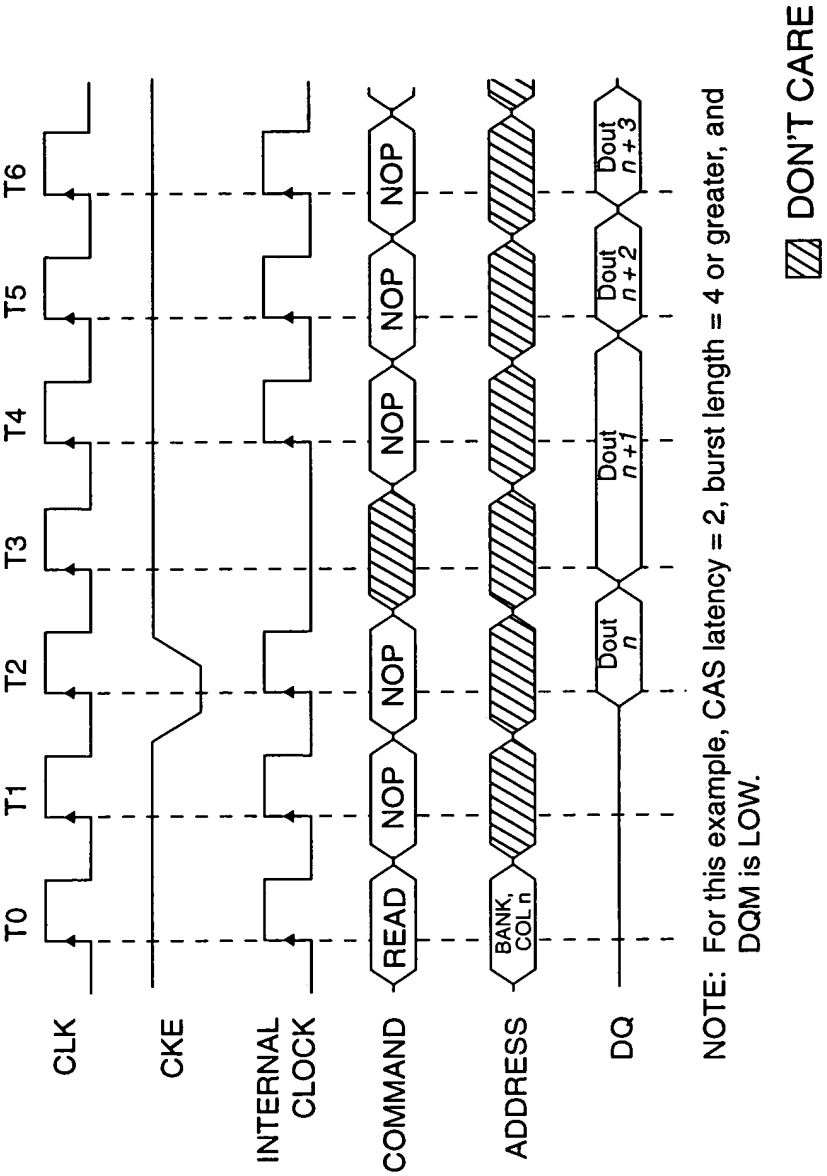


Fig. 14

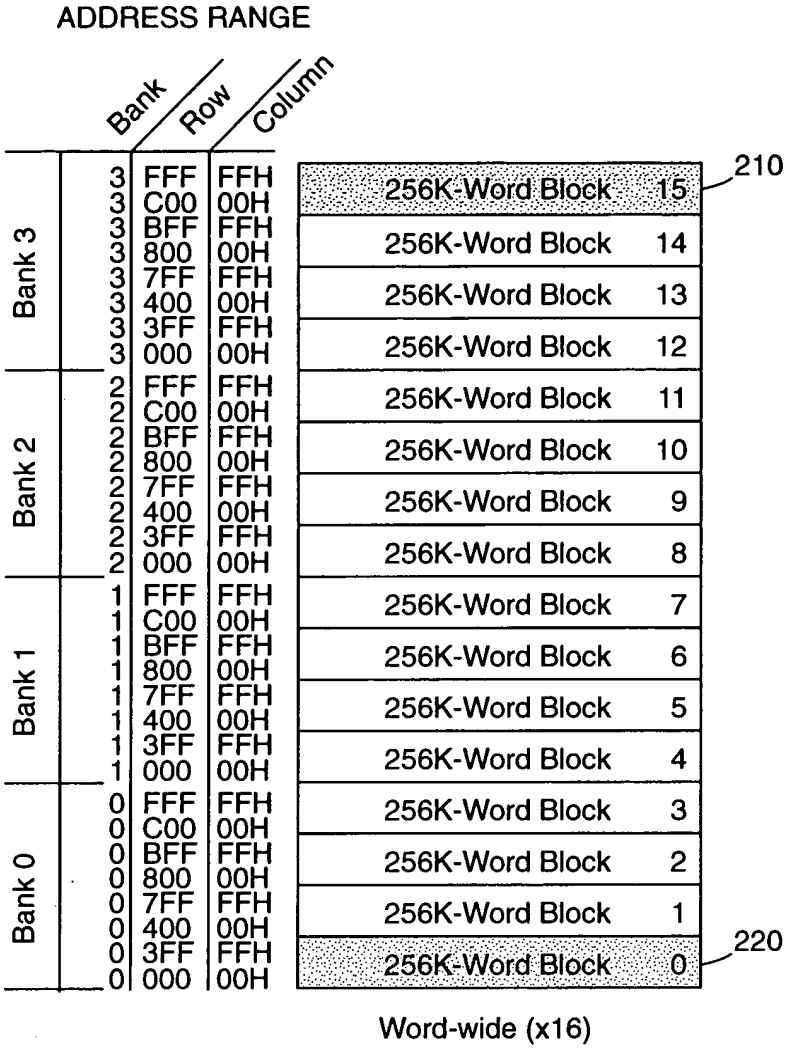


Fig. 15

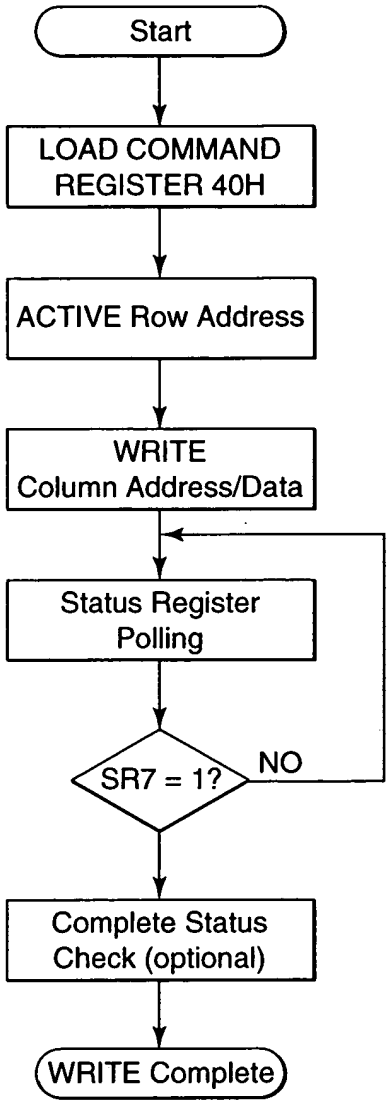


Fig. 16

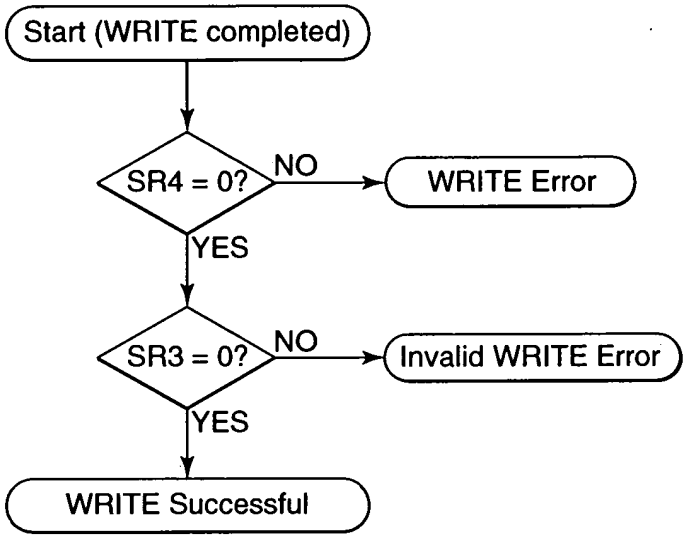


Fig. 17

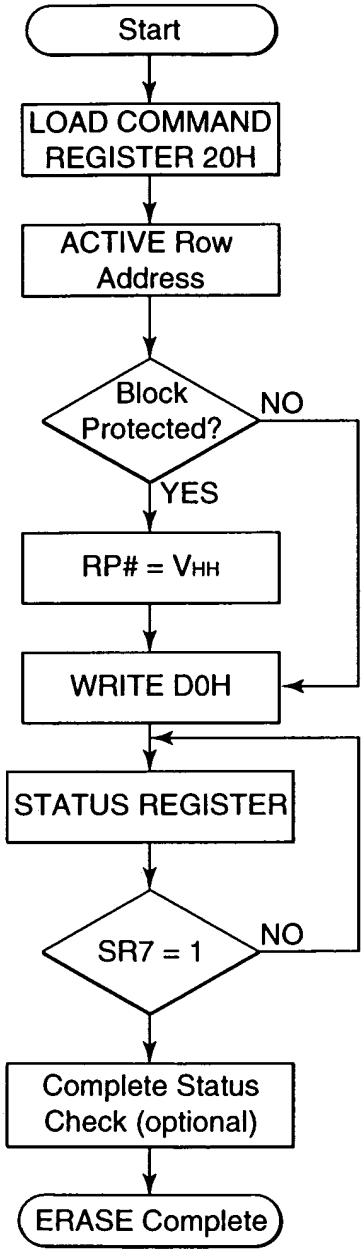


Fig. 18

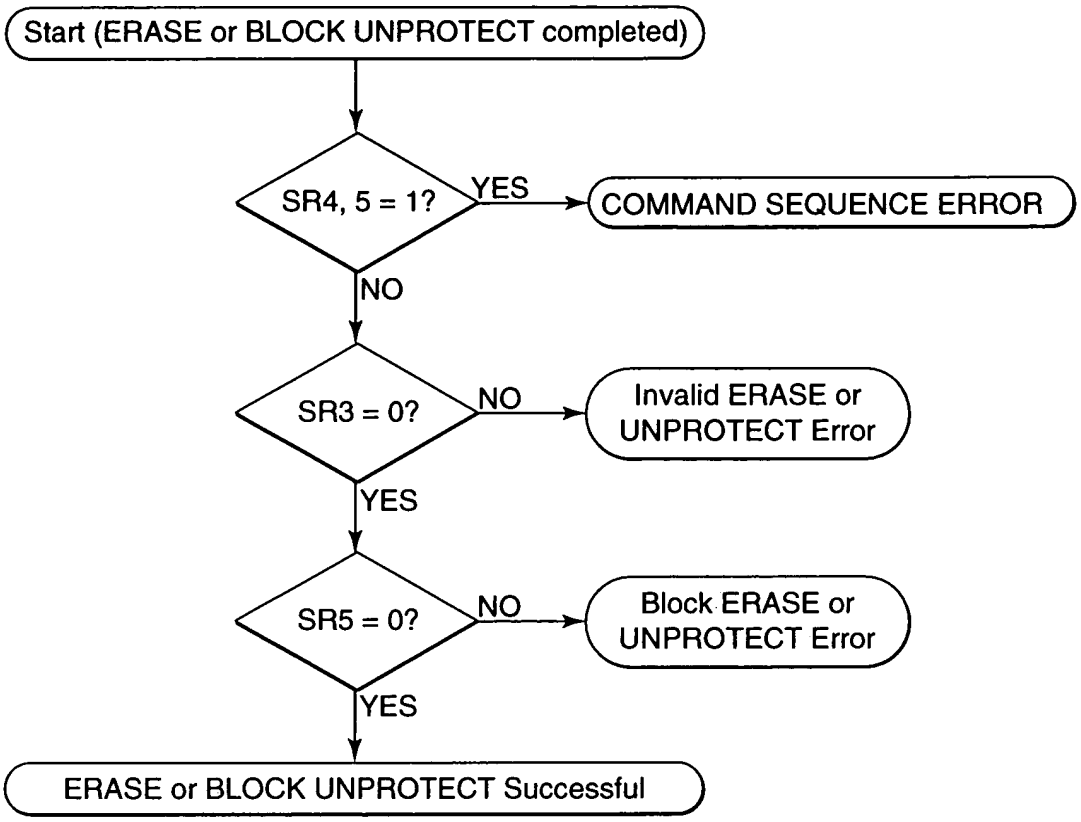


Fig. 19

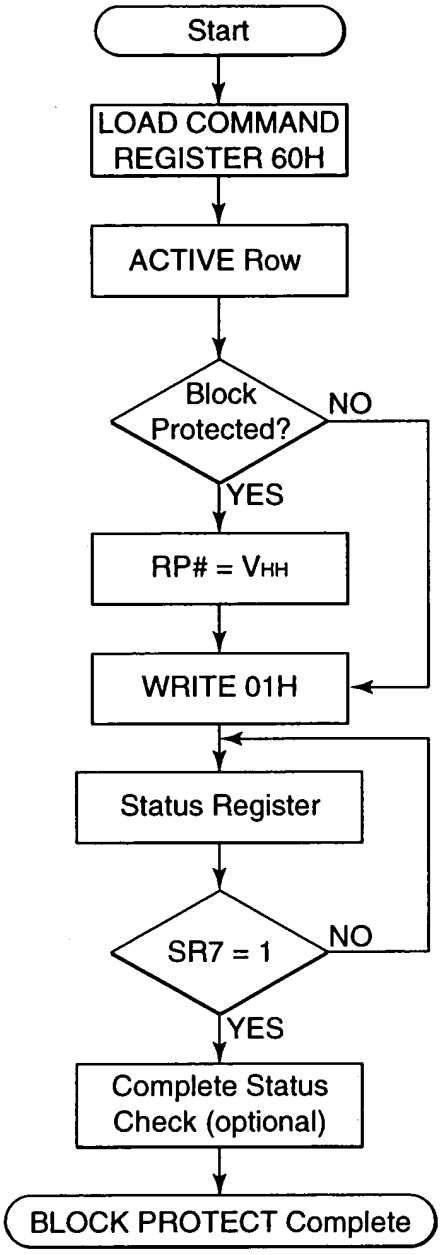


Fig. 20

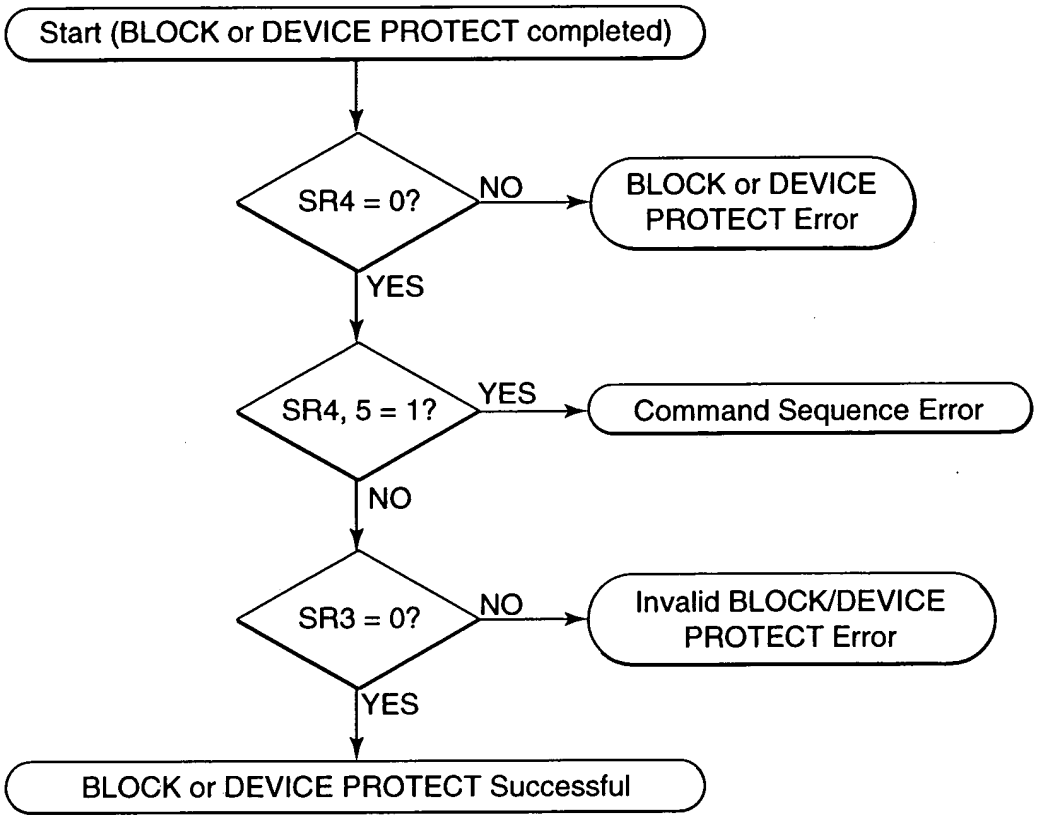


Fig. 21

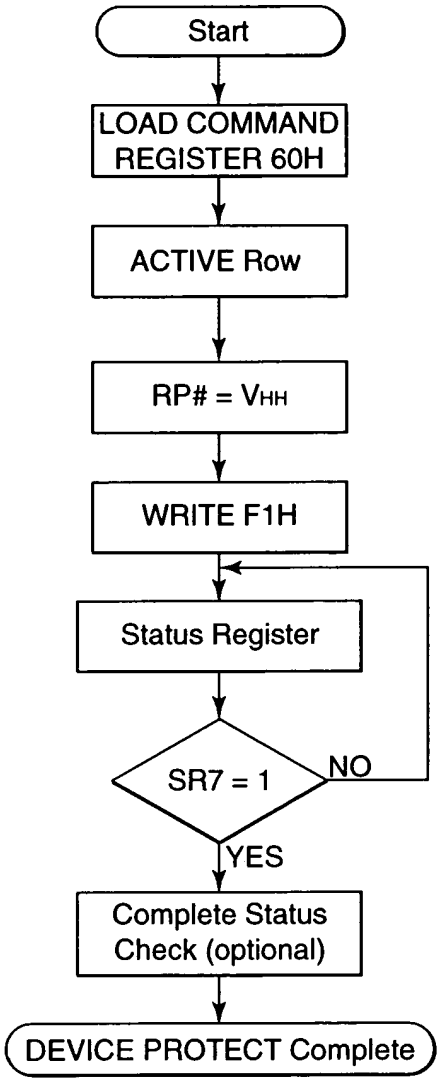


Fig. 22

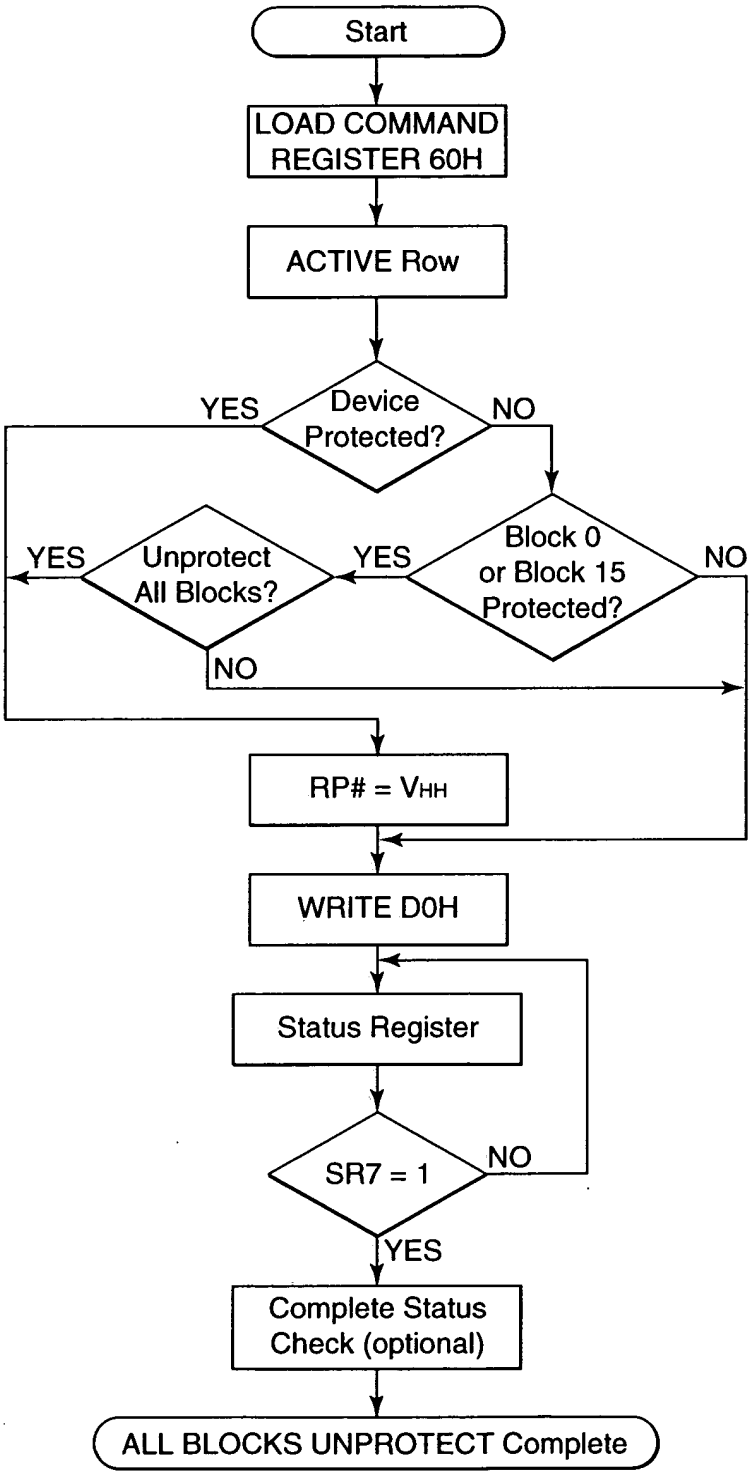


Fig. 23

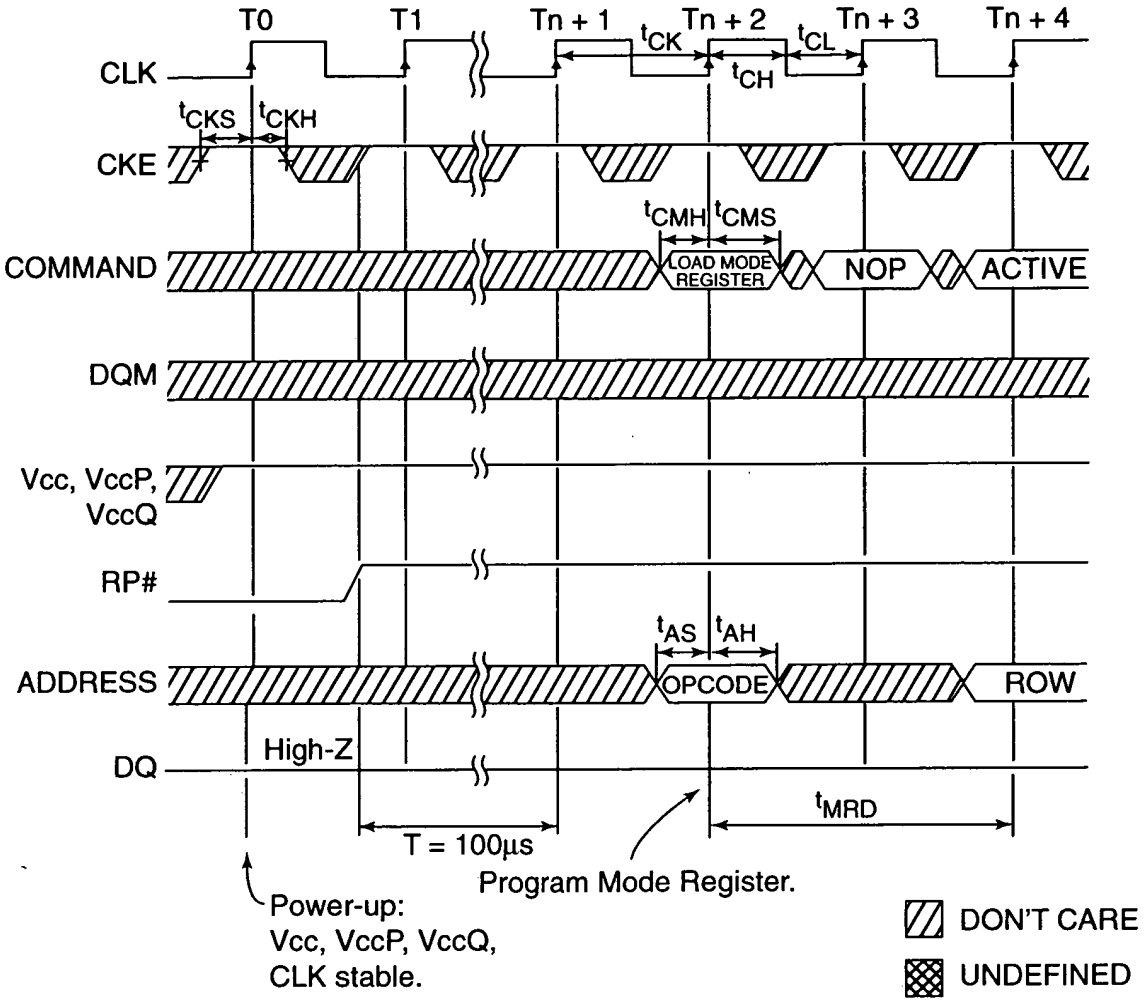


Fig. 24

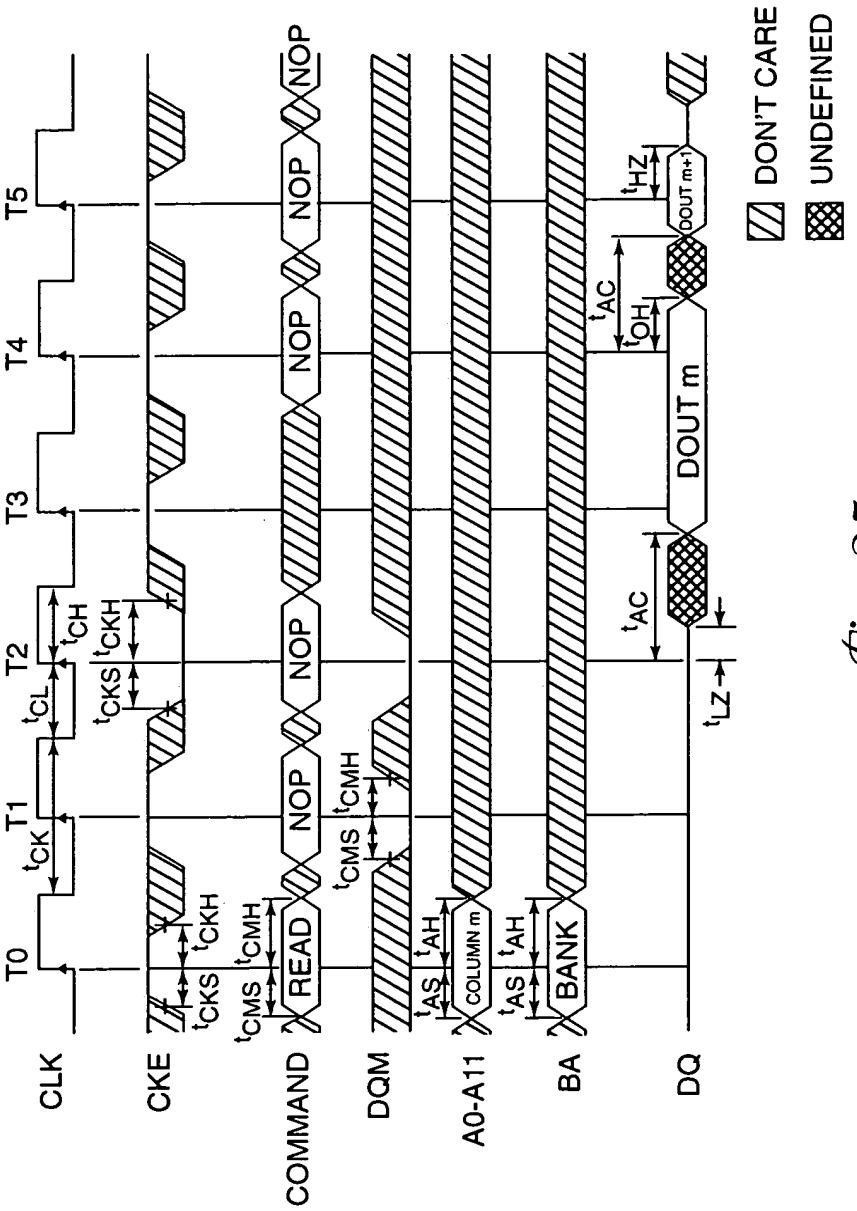


Fig. 25

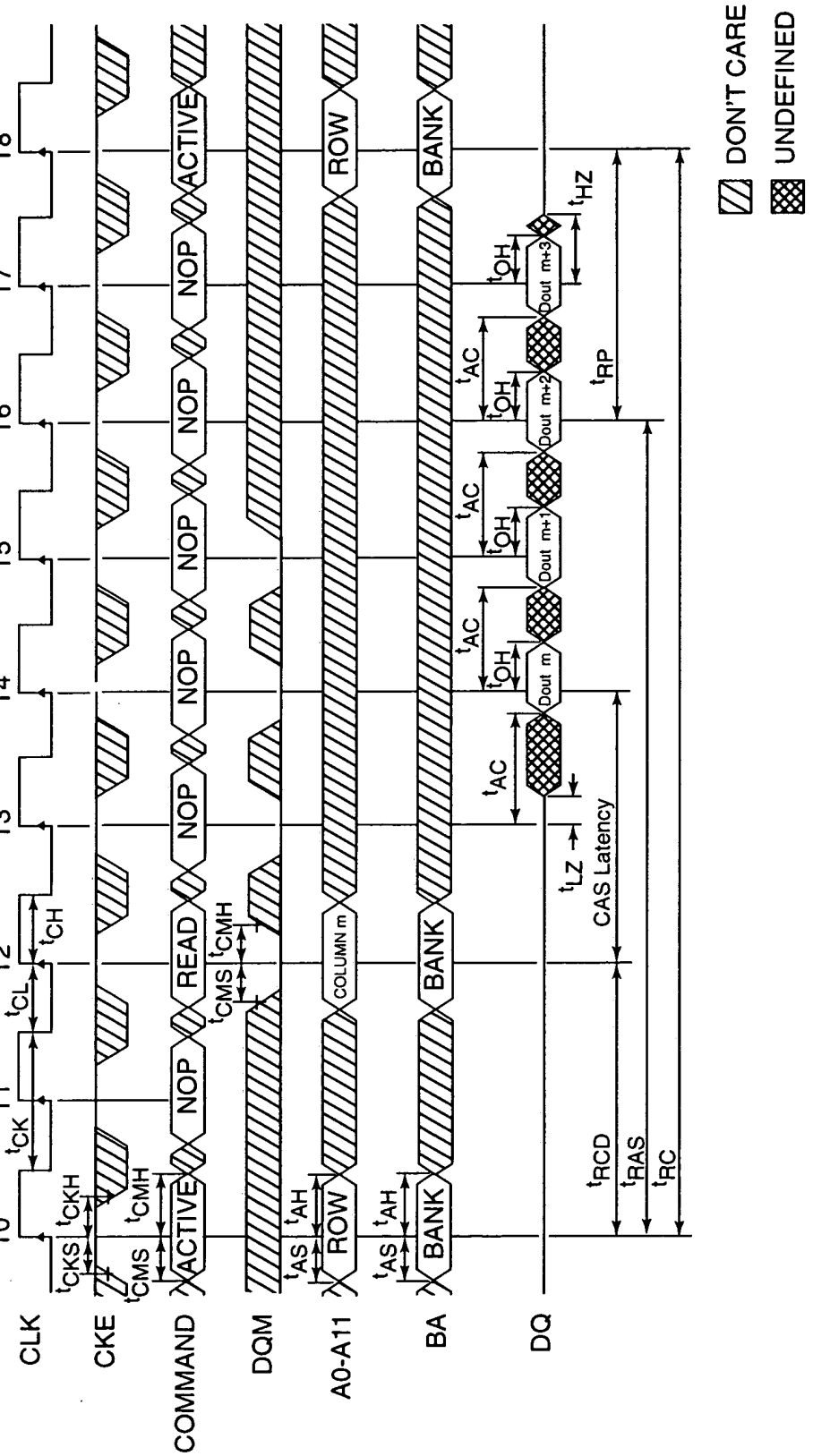


Fig. 26

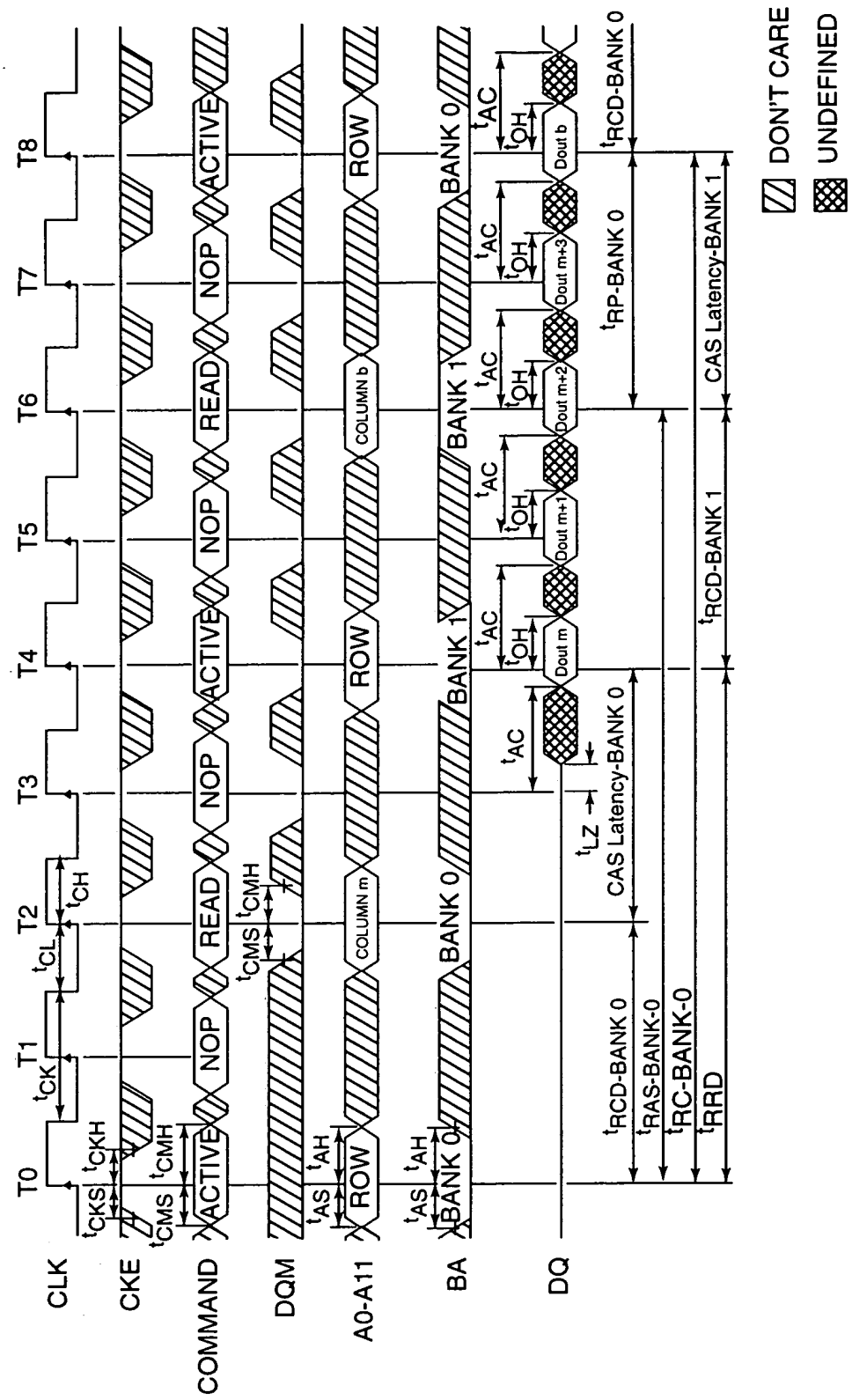


Fig. 27

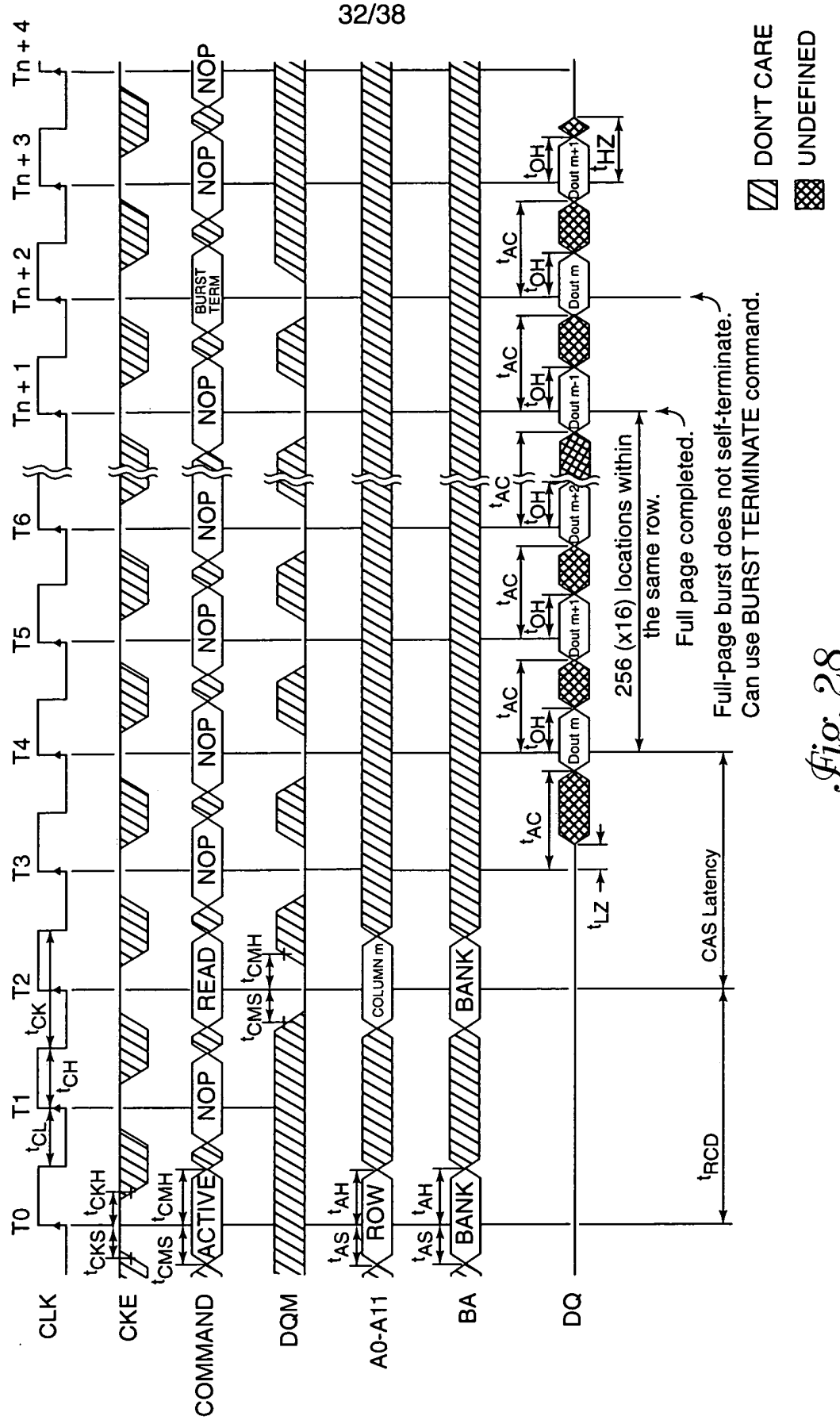


Fig. 28

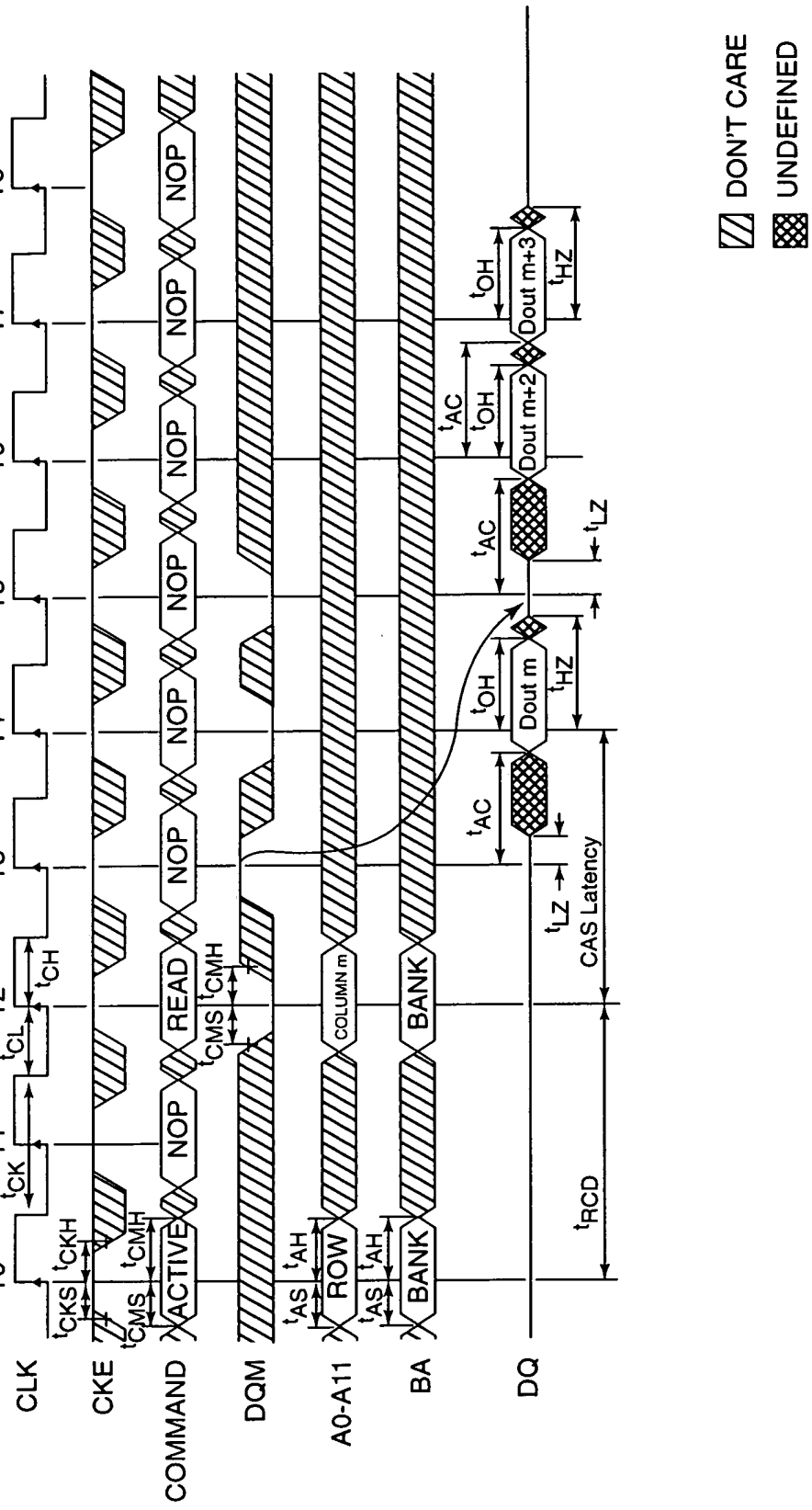


Fig. 29

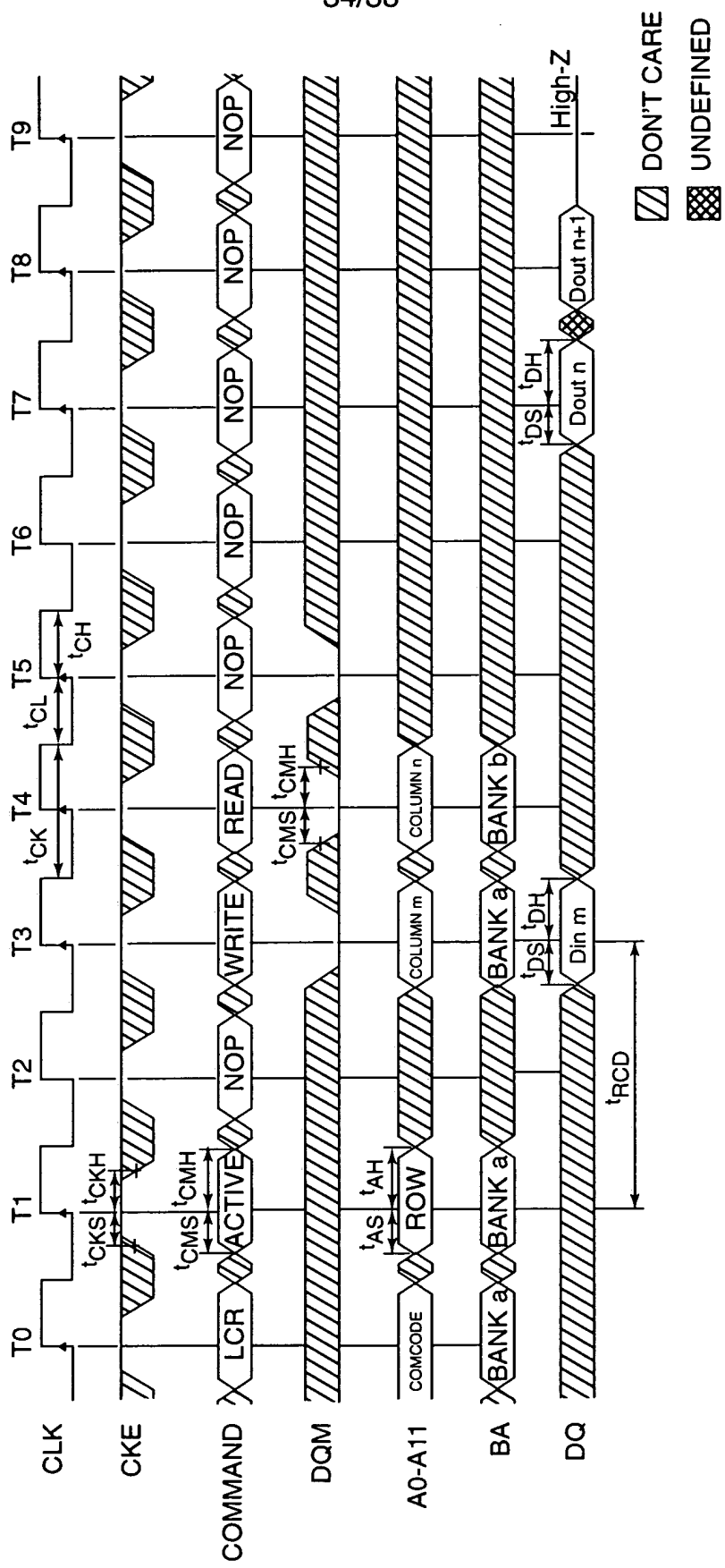
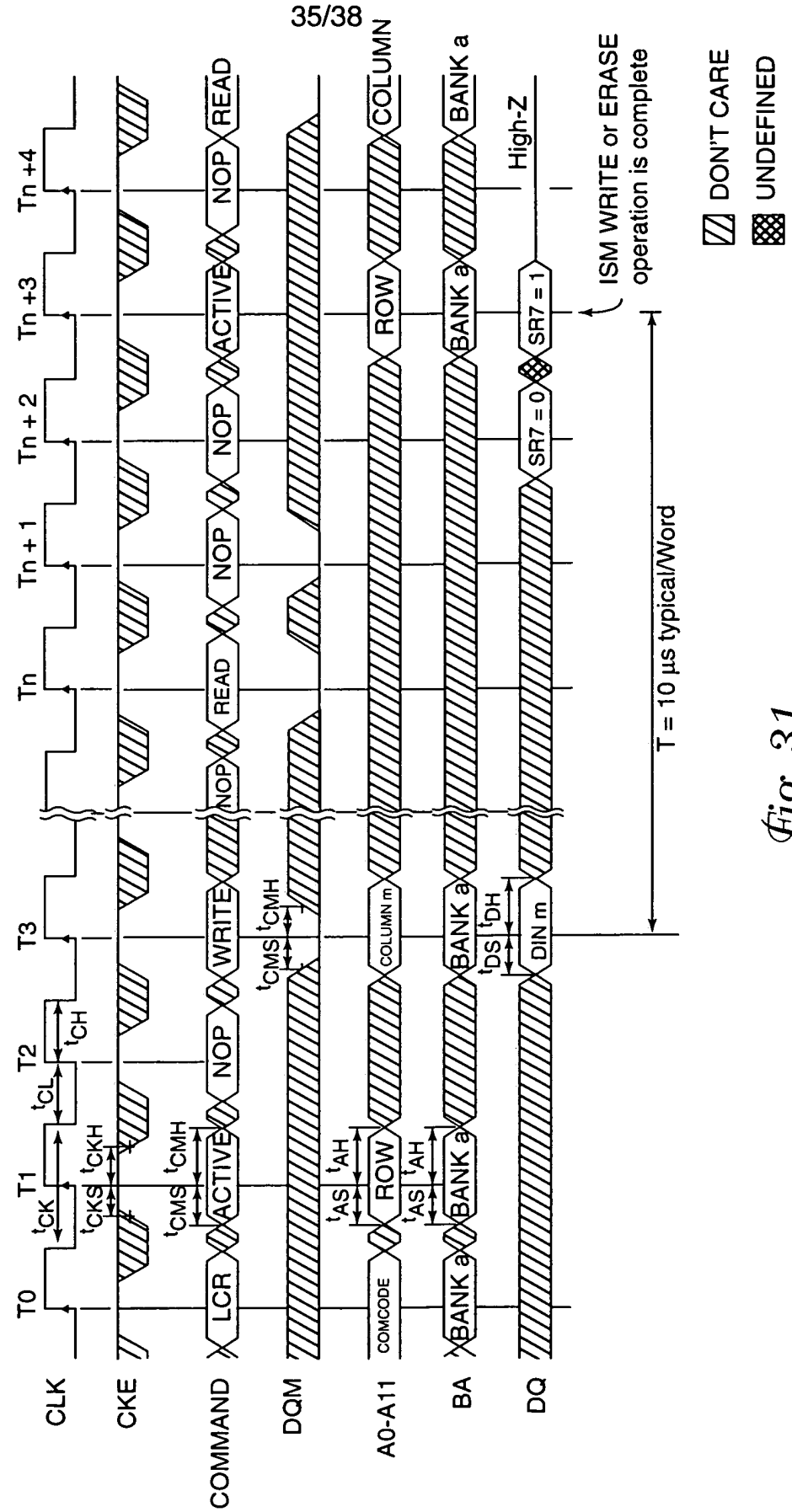


Fig. 30



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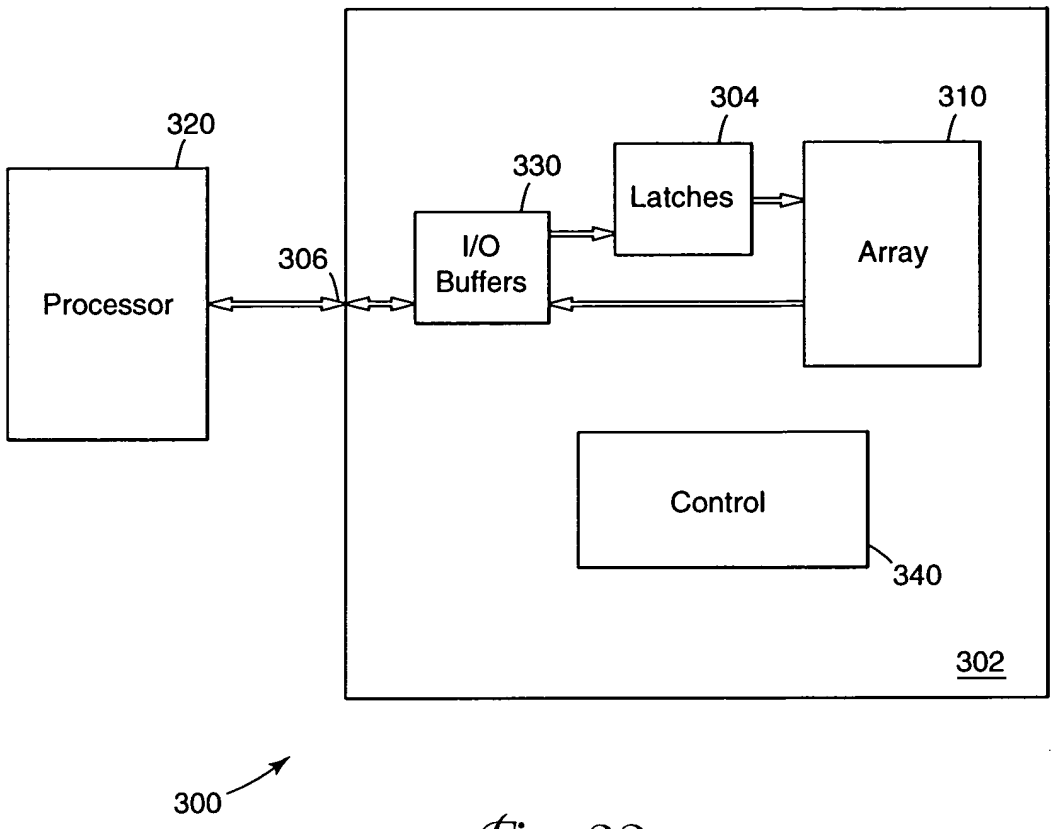

Fig. 32

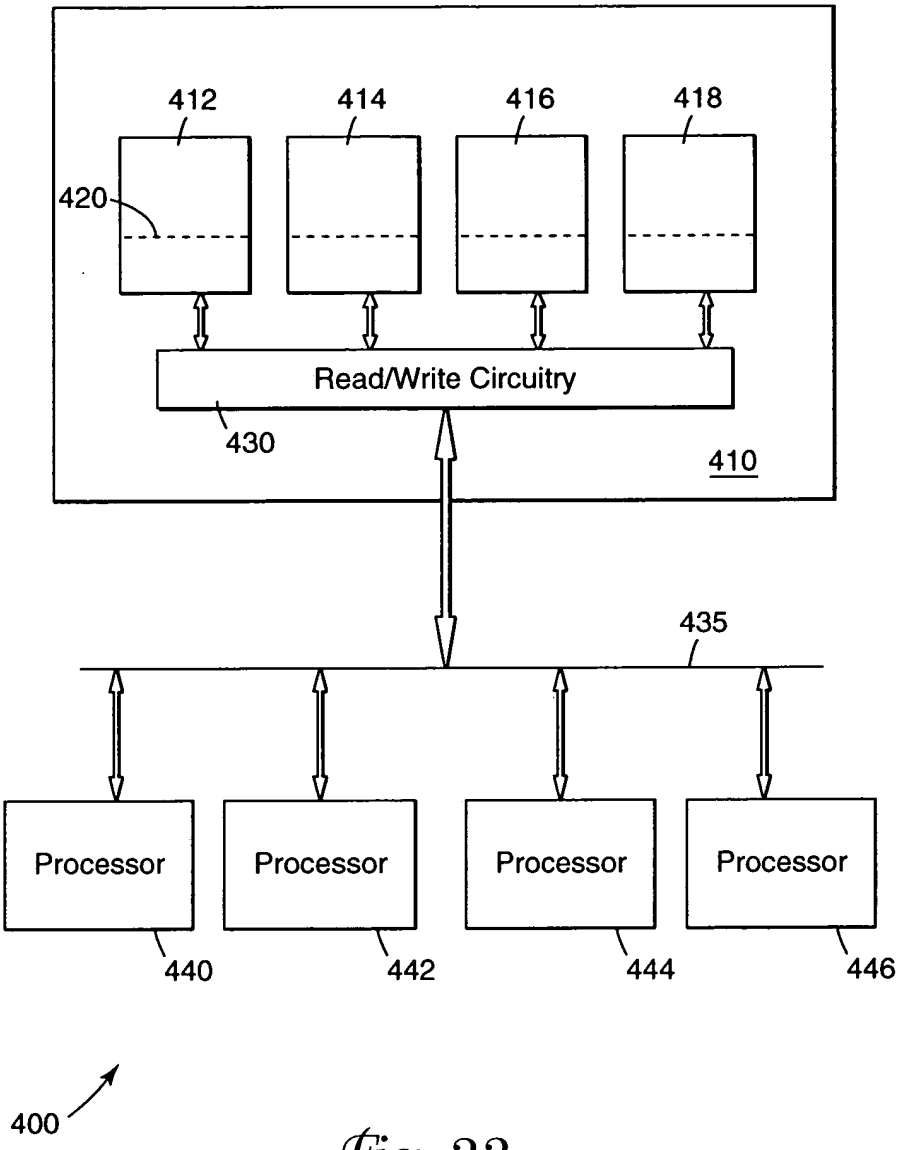

Fig. 33

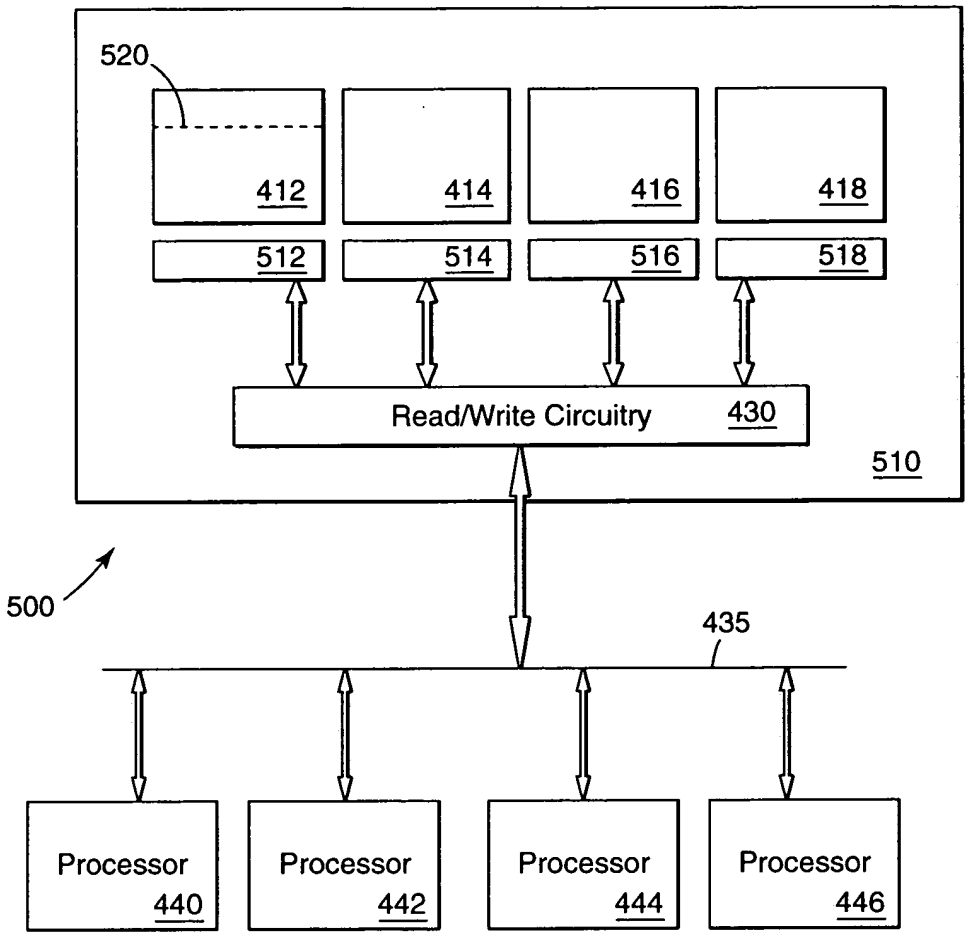

Fig. 34